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Coding and decoding methods for a picture signal using overlapping blocks having variable block sizes and apparatuses therefor.

Abstract:

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The described method and apparatus for coding and decoding a bidimensional signal of the present 139a invention comprises at a coding side means for dividing a bidimensional input signal into rectangular blocks of a plurality of sizes, providing window functions $hAM(m)$ and $hBN(n)$ on a direction and block size basis, means for calculating, assuming that a block of interest has an $M \times N$ size, a transformed signal $Xp(k, i)$ from input signals $xp(m, n)$ by means of a particular transform equation. The block division information and the transformed signals are transmitted to a decoding side. At the decoding side window functions $fAM(m)$ and $fBN(n)$ corresponding to the window functions $hAM(m)$ and $hBN(n)$, respectively, are provided for calculating, when a block size of the transformed signals sent from the coding side is $M \times N$, inverse-transformed signals $yp(m, n)$ of $2M \times 2N$ points from the transformed signals. The inverse-transformed signals are arranged according to the block division information sent from the coding side, and the inverse-transformed signals are added, in a portion where a plurality of blocks overlap to produce a decoded signal. This system splits a bidimensional picture signal into picture blocks and is capable of decoding an input signal correctly even when blocks overlap and have a variable block size.

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(54) **Coding and decoding methods for a picture signal using overlapping blocks having variable block sizes and apparatuses therefor.**

(57) The described method and apparatus for coding and decoding a bidimensional signal of the present invention comprises at a coding side means for dividing a bidimensional input signal into rectangular blocks of a plurality of sizes, providing window functions $h_{AM}(m)$ and $h_{BN}(n)$ on a direction and block size basis, means for calculating, assuming that a block of interest has an $M \times N$ size, a transformed signal $X_p(k, i)$ from input signals $x_p(m, n)$ by means of a particular transform equation. The block division information and the transformed signals are transmitted to a decoding side. At the decoding side window functions $f_{AM}(m)$ and $f_{BN}(n)$ corresponding to the window functions $h_{AM}(m)$ and $h_{BN}(n)$, respectively, are provided for calculating, when a block size of the transformed signals sent from the coding side is $M \times N$, inverse-transformed signals $y_p(m, n)$ of $2M \times 2N$ points from the transformed signals. The inverse-transformed signals are arranged according to the block division information sent from the coding side, and the inverse-transformed signals are added, in a portion where a plurality of blocks overlap to produce a decoded signal. This system splits a bidimensional picture signal into picture blocks and is capable of decoding an input signal correctly even when blocks overlap and have a variable block size.

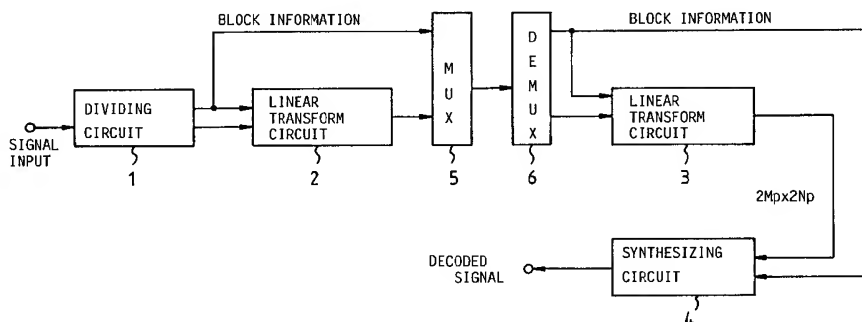


Fig. 7

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BACKGROUND OF THE INVENTION

The present invention relates to methods and apparatuses for coding and decoding a picture signal or similar bidimensional signal block by block.

It is a common practice with the above-mentioned type of coding and decoding to divide a bidimensional signal into blocks each having a predetermined block size and then code and decode it on a block basis. The problem with this kind of coding and decoding scheme is that discontinuous changes in the signal, i.e., so-called block boundary artifacts occur at the boundaries of adjoining blocks. To reduce block boundary artifacts, a method which uses blocks having overlapping regions has been taught by, for example, H. Schiller in "Overlapping block transform for image coding preserving equal number of samples and coefficients", SPIE, Visual communications and Image Processing '88, November 9-11. Schiller divides an image signal into blocks each having $2N \times 2N$ pixels (N being a natural number) and overlapping adjacent blocks by half the block size in four directions which are perpendicular to the four sides thereof. An input pixel signal representative of $2N \times 2N$ points is coded into a transformed signal representative of $N \times N$ points by transform equations which will be described later, and then the transformed signal is transmitted. The $N \times N$ transformed signal is transformed into a $2N \times 2N$ inverse-transformed signal by an inverse procedure. Subsequently, this inverse-transformed signal and the inverse-transformed signals of the associated regions of the overlapping blocks are added to produce a decoded signal. A similar implementation is taught in H.S. Malvar et al "The LOT: Transform Coding Without Blocking Effects", IEEE TRANSACTIONS ON ACOUSTICS, SPEECH, AND SIGNAL PROCESSING, Vol. 37, No. 4, April 1989. On the other hand, it is known that when a bidimensional picture signal is divided into blocks and then coded and decoded, increasing the block size in regions where the change is insignificant and decreasing it in a region where the change is significant is successful in enhancing efficient coding and decoding. For example, Cheng-Tie Chen reported a unique procedure which splits a picture signal into non-overlapping bidimensional blocks of variable sizes and effects transform and coding in each of the blocks by Discrete Cosine Transform (DCT) in "Adaptive Transform Coding Via Quadtree-Based Variable Block Size DCT" at 1989 INTERNATIONAL CONFERENCE ON ACOUSTICS, SPEECH, AND SIGNAL PROCESSING, Volume 3, MULTIDIMENSIONAL SIGNAL PROCESSING AUDIO & ELECTROACOUSTICS, held in Glasgow, May 23-26, 1989. Specifically, a picture signal is divided into $M \times M$ relatively large blocks, and then each blocks is subdivided into $N \times N$ ($N = M/2^i$; i being a natural number) unit subblocks whose number is the power of "4". Subsequently, the mean value of pixel levels is determined with each of four unit subblocks which constitute a $2N \times 2N$ block. If the differences ($4C_2 = 6$) among such unit subblocks are smaller than a predetermined value, the unit subblocks are combined into a $2N \times 2N$ intermediate subblock and then dealt with as one of four $2N \times 2N$ intermediate subblocks that constitute an $8N \times 8N$ block. In this manner, the block size is sequentially increases so long as the conditions are satisfied. As a result, $(i + 1)$ kinds of block sizes at maximum, i.e., $N \times N$ to $M \times M$ block sizes may be generated, depending on the degree to which the conditions are satisfied. Coding and decoding are effected with each of such blocks to enhance efficiency.

However, the Chen's coding and decoding scheme suffers from the previously stated block boundary artifacts since it divides an input signal into blocks such that the blocks do not overlap one another. The Schiller's method and Malvar's method both are not practicable unless blocks of the same size overlap, i.e., correct results of decoding are not achievable when blocks of different sizes are used.

SUMMARY OF THE INVENTION

It is therefore an object of the present invention to provide a coding and decoding method of the type splitting a bidimensional picture signal into picture blocks and coding and decoding on a block basis and capable of decoding an input signal correctly even when blocks overlap and have a variable block size.

It is another object of the present invention to provide a coding apparatus, decoding apparatus, and coding and decoding apparatus to which a coding and decoding method of the present invention is applied.

A method of coding and decoding a bidimensional signal of the present invention comprises the steps of, at a coding side, dividing a bidimensional input signal into rectangular blocks of a plurality of sizes, providing window functions $h_{AM}(m)$ and $h_{BN}(n)$ on a direction and block size basis, calculating, assuming that a block of interest has an $M \times N$ size, transformed signal $X_p(k, i)$ from input signals $x_p(m, n)$ ($m = 0, 1, \dots, 2M-1$; $n = 0, 1, \dots, 2N-1$) by a transform equation:

$$\begin{aligned}
 x_p(k, i) = & \sum_{m=0}^{2M-1} \sum_{n=0}^{2N-1} x_p(m, n) h_{AM}(m) h_{BN}(n) \\
 & \times \cos \left\{ (2m + M + 1) (2k + 1) \pi / (4M) \right\} \\
 & \times \cos \left\{ (2n + N + 1) (2i + 1) \pi / (4N) \right\} \\
 & (k = 0, 1, \dots, M-1; \quad i = 0, 1, \dots, N-1)
 \end{aligned}$$

and transmitting block division information and the transformed signals to a decoding side, and at the decoding side providing window function $f_{AM}(m)$ and $f_{BN}(n)$ corresponding to the window functions $h_{AM}(m)$ and $h_{BN}(n)$, respectively, calculating, when a block size of the transformed signals sent from the coding side is $M \times N$, inverse-transformed signals $y_p(m, n)$ of $2M \times 2N$ points from the transformed signals by using a transform equation:

$$\begin{aligned}
 y_p(m, n) = & \sum_{k=0}^{M-1} \sum_{i=0}^{N-1} x_p(k, i) f_{AM}(m) f_{BN}(n) / (MN) \\
 & \times \cos \left\{ (2m + M + 1) (2k + 1) \pi / (4M) \right\} \\
 & \times \cos \left\{ (2n + N + 1) (2i + 1) \pi / (4N) \right\} \\
 & (m = 0, 1, \dots, 2M-1; \quad n = 0, 1, \dots, 2N-1)
 \end{aligned}$$

arranging the inverse-transformed signals according to the block division information sent from the coding side, and adding, in a portion where a plurality of blocks overlap, the inverse-transformed signals to produce a decoded signal.

BRIEF DESCRIPTION OF THE DRAWINGS

The above-mentioned and other objects, features and advantages of the present invention will become more apparent from the following detailed description when taken in conjunction with the accompanying drawings, wherein:

- Figs. 1a-1c and Fig. 2 show a specific conventional relation of overlapping blocks to one another;
- Figs. 3a-3c show regions of an input signal used in calculating transformed signals in accordance with the present invention;
- Figs. 4a-4c show window functions;
- Figs. 5a-5d illustrate how a composite signal of overlapping regions of two blocks is calculated;
- Fig. 6a shows blocks contacting from opposite side with respect to a given line;
- Fig. 6b illustrates how a composite signal of overlapping regions of two blocks is calculate;
- Fig. 7 is a block diagram schematically showing apparatuses for coding and decoding a bidimensional signal embodying the present invention;
- Figs. 8a and 8b each shows a specific block division principle;
- Figs. 9a and 9b are block diagrams each schematically showing a specific construction of a decision circuit;
- Fig. 10a is a block diagram schematically showing a specific construction of a linear transform circuit 2;
- Fig. 10b is a schematic block diagram showing a specific construction of a linear transform circuit 3;
- Fig. 11a is a block diagram showing a specific construction of a window function generating circuit 17 included in the linear transform circuit 2;

Fig. 11b is a block diagram schematically showing a specific construction of a window function generating circuit 27 included in the linear transform circuit 3;

Fig. 12 is a table listing a relation between the output of a data counter 51 and the output of a counter 34 shown in Fig. 11a;

5 Fig. 13 is a table listing the addresses of a ROM 35 shown in Fig. 11a and specific contents stored therein; and

Fig. 14 is a block diagram showing a specific construction of a synthesizing circuit 4 shown in Fig. 7.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

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To better understand the present invention, a brief reference will be made to the conventional method taught by Schiller, shown in Figs. 1a-1c. The figures show only a given block of interest (block P) and other blocks (blocks 1-8) overlapping the block P, for the sake of simplicity of description. As shown, the block P is constituted by a $2N \times 2N$ pixel signal and overlaps blocks 7 and 8 (Fig. 1c) and blocks 5 and 6 (Fig. 1b) in directions x and y, respectively, by half the block size each. Further, the block P overlaps blocks 1 and 3 which overlap the block 5 in the direction x by half the block size and blocks 2 and 4 which overlap the block 6 in the direction x by half the block size, over an $N \times N$ region (Fig. 1a). Hence, the block P overlaps with eight blocks in total. The blocks shown in Figs. 1a-1c appear as shown in Fig. 2 when seen from the above.

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Assume that the input $2N \times 2N$ pixel signal representative of the block P of interest is $x_p(m, n)$ ($m = 0, 1, \dots, 2N-1$; $n = 0, 1, \dots, 2N-1$). Then, the input $2N \times 2N$ pixel signal is coded into an $N \times N$ transformed signal $X_p(k, i)$ by a predetermined window function $W(n)$ ($n = 0, 1, \dots, 2N-1$) and an equation:

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$$X_p(k, i) = \sum_{m=0}^{2N-1} \sum_{n=0}^{2N-1} x_p(m, n) \cdot W(m) \cdot W(n) \cdot \cos \left\{ (2m + N + 1) \cdot (2k + 1) \pi / (4N) \right\} \cdot \cos \left\{ (2n + N + 1) \cdot (2i + 1) \pi / (4N) \right\} \dots (1)$$

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$$(k = 0, 1, \dots, N-1; \quad i = 0, 1, \dots, N-1)$$

The transformed signal $X_p(k, i)$ is transmitted. The equation (1) corresponds to subsampling signals at $2N \times 2N$ points in the frequency domain. For decoding, inverse-transformed signals are produced from the transformed signals $X_p(k, i)$ by the window function $W(n)$ used for coding and an equation:

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$$y_p(m, n) = \sum_{k=0}^{N-1} \sum_{i=0}^{N-1} X_p(k, i) W(m) W(n) / N^2 \cdot \cos \left\{ (2m + N + 1) (2k + 1) \pi / (4N) \right\} \cdot \cos \left\{ (2n + N + 1) (2i + 1) \pi / (4N) \right\} \dots (2)$$

$$(m = 0, 1, \dots, 2N-1; \quad n = 0, 1, \dots, 2N-1)$$

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The inverse-transformed signals of all the blocks which occupy the same space are added to produce a decoded signal. As shown in Fig. 2, any region inside the block P overlies four blocks. Hence, the inverse-transformed signals corresponding to the same regions of the overlapping four blocks are added to produce

a decoded signal. Decoded signals at the block boundaries can also be produced if the inverse-transformed signals of the plurality of blocks are added, whereby block boundary artifacts are reduced. Since the blocks overlap, decoding is possible even when signals at $2N \times 2N$ points are sampled at $N \times N$ points. Complete decoding (neglecting quantization errors at the time of coding and transmission) is achievable if the window function $W(n)$ satisfies the following conditions:

Condition 1 - the window function is symmetrical in the right-and-left direction.

Condition 2 - the sum of power is always 1 (one) when turned back at the center of each of the symmetrical right and left regions, at points which are one-fourth of the entire window function from the right and left ends.

Expressing the above conditions in equations,

$$\text{Condition 1: } W(n) = W(2N - 1 - n) \\ (n = 0, 1, \dots, N - 1)$$

$$\text{Condition 2: } W^2(n) + W^2(N - 1 - n) = 1 \\ (n = 0, 1, \dots, N/2 - 1)$$

The problem with such a window function is that it cannot be used unless the blocks overlapping one another by half the block size are identical in size.

The present invention divides a bidimensional input picture signal into a plurality of blocks each having a particular size by use of some basis. Specifically, the basis to be used may be implemented as the previously mentioned Chen's method or any other method which is selected by comparing the amounts of code, the lower range power intensity, etc. Assuming that a block of interest (hereinafter referred to as a block P) has an M (horizontal) \times N (vertical) block size, the present invention performs coding by using input signals representative of a $2M \times 2N$ range centering around the block P. Assuming that input signals of the $2M \times 2N$ points are $x_p(m, n)$ ($m = 0, 1, \dots, 2M-1$; $n = 0, 1, \dots, 2N-1$), then they are coded into coded signals $X_p(k, i)$ of $M \times N$ points, as follows:

$$X_p(k, i) = \sum_{m=0}^{2M-1} \sum_{n=0}^{2N-1} x_p(m, n) h_{AM}(m) h_{BN}(n) \\ \times \cos \left\{ (2m + M + 1) (2k + 1) \pi / (4M) \right\} \\ \times \cos \left\{ (2n + N + 1) (2i + 1) \pi / (4N) \right\} \\ (k = 0, 1, \dots, M-1; i = 0, 1, \dots, N-1) \\ \dots (3)$$

where $h_{AM}(m)$ and $h_{BN}(n)$ are window functions prepared beforehand on the basis of horizontal and vertical directions (A, B) and block sizes (M, N).

The transformed signal $X_p(k, i)$ is transmitted together with block division information which may be, for example, the origin and the horizontal and vertical sizes of the block. Regarding decoding, the present invention produces an inverse-transformed signal $y_p(k, i)$ of the transformed signal $x_p(k, i)$ by using window functions $f_{AM}(m)$ and $f_{BN}(n)$ prepared beforehand in association with the above-mentioned window functions $h_{AM}(m)$ and $h_{BN}(n)$ and an equation:

$$y_p(m, n) = \sum_{k=0}^{M-1} \sum_{i=0}^{N-1} x_p(k, i) f_{AM}(m) f_{BN}(n) / (MN)$$

$$\times \cos \left\{ (2m + M + 1) (2k + 1) \pi / (4M) \right\}$$

$$\times \cos \left\{ (2n + N + 1) (2i + 1) \pi / (4N) \right\}$$

$$(m = 0, 1, \dots, 2M-1; n = 0, 1, \dots, 2N-1)$$

.... (4)

Subsequently, the inverse-transformed signals $y_p(m, n)$ are arranged on the basis of the block division information sent from the coding side. In each portion where a plurality of blocks overlap, the inverse-transformed signals are added to produce a decoded signal. As shown in Fig. 3b, let the $2M \times 2N$ block be divided into four regions 1-4 for illustration. Assume that transformed signals $X_p(k, i)$ without any block boundary artifacts are decoded. Then, by substituting the equation (3) for the equation (4), the inverse-transformed signals $y_p(m, n)$ in the regions 1-4 are expressed by use of the input signals $x_p(m, n)$, as follows.

In the region 1 ($m = 0, 1, \dots, M-1; n = 0, 1, \dots, N-1$)

$$\begin{aligned} y_p(m, n) = & x_p(m, n) h_{AM}(m) f_{AM}(m) h_{BN}(n) f_{BN}(n) \\ & - x_p(M-1-m, n) h_{AM}(M-1-m) h_{AM}(m) h_{BN}(n) f_{BN}(n) \\ & - x_p(m, N-1-n) h_{AM}(m) f_{AM}(m) h_{BN}(N-1-n) f_{BN}(n) \\ & + x_p(M-1-m, N-1-n) h_{AM}(M-1-m) f_{AM}(m) \\ & \quad \times h_{BN}(N-1-n) f_{BN}(n) \end{aligned} \quad \dots (5.a)$$

In the region 2 ($m = M, M+1, \dots, 2M-1; n = 0, 1, \dots, N-1$)

$$\begin{aligned} y_p(m, n) = & x_p(m, n) h_{AM}(m) f_{AM}(m) h_{BN}(n) f_{BN}(n) \\ & + x_p(3M-1-m, n) h_{AM}(3M-1-m) f_{AM}(m) h_{BN}(n) f_{BN}(n) \\ & - x_p(m, N-1-n) h_{AM}(m) f_{AM}(m) h_{BN}(N-1-n) f_{BN}(n) \\ & - x_p(3M-1-m, N-1-n) h_{AM}(3M-1-m) f_{AM}(m) \\ & \quad \times h_{BN}(N-1-n) f_{BN}(n) \end{aligned} \quad \dots (5.b)$$

In the region 3 ($m = 0, 1, \dots, M-1; n = N, N+1, \dots, 2N-1$)

$$\begin{aligned}
y_p(m, n) = & x_p(m, n) h_{AM}(m) f_{AM}(m) h_{BN}(n) f_{BN}(n) \\
& - x_p(M-1-m, n) h_{AM}(M-1-m) f_{AM}(m) h_{BN}(n) f_{BN}(n) \\
& + x_p(m, 3N-1-n) h_{AM}(m) f_{AM}(m) h_{BN}(3N-1-n) f_{BN}(n) \\
& - x_p(M-1-m, 3N-1-n) h_{AM}(M-1-m) f_{AM}(m) \\
& \quad \times h_{BN}(3N-1-n) f_{BN}(n) \quad \dots (5.c)
\end{aligned}$$

In the region 4
 $(m = M, M+1, \dots, 2M-1)$
 $(n = N, N+1, \dots, 2N-1)$

$$\begin{aligned}
y_p(m, n) = & x_p(m, n) h_{AM}(m) f_{AM}(m) h_{BN}(n) f_{BN}(n) \\
& + x_p(3M-1-m, n) h_{AM}(3M-1-m) f_{AM}(m) h_{BN}(n) f_{BN}(n) \\
& + x_p(m, 3N-1-n) h_{AM}(m) f_{AM}(m) h_{BN}(3N-1-n) f_{BN}(n) \\
& + x_p(3M-1-m, 3N-1-n) h_{AM}(3M-1-m) f_{AM}(m) \\
& \quad \times h_{BN}(3N-1-n) f_{BN}(n) \quad \dots (5.d)
\end{aligned}$$

On the other hand, assume that the minimum block size is $M_0 \times N_0$ that the window functions of the coding side which correspond to the horizontal direction M_0 and the vertical direction N_0 are $h_{AMO}(m)$ and $h_{BNO}(n)$, respectively, and that the window functions at the decoding side are $f_{AMO}(m)$ and $f_{BNO}(n)$. Then, in order that input signals equally divided into minimum blocks may be regenerated, the following conditions should be satisfied: as to window functions $h_{AMO}(m)$ and $f_{AMO}(m)$ in horizontal direction

$$\begin{aligned}
h_{AMO}(m) f_{AMO}(m) + h_{AMO}(m+M_0) f_{AMO}(m+M_0) &= 1 \\
(0 \leq m < M_0) &\dots (6.a)
\end{aligned}$$

$$\begin{aligned}
h_{AMO}(2M_0-1-m) f_{AMO}(m+M_0) &= h_{AMO}(M_0-1-m) f_{AMO}(m) \\
(0 \leq m < M_0) &\dots (6.b)
\end{aligned}$$

as to window functions $h_{BNO}(n)$ and $f_{BNO}(n)$

$$h_{BNO}(n) f_{BNO}(n) + h_{BNO}(n+N_0) f_{BNO}(n+N_0) = 1$$

$$(0 \leq n < N_0) \quad \dots \quad (5.c)$$

$$h_{BNO}(2N_0 - 1 - n) f_{BNO}(n + N_0) = h_{BNO}(N_0 - 1 - n) f_{BNO}(n)$$

$$(0 \leq n < N_0) \quad \dots \quad (5.d)$$

The window functions $h_{AM}(m)$, $h_{BN}(n)$, $f_{AM}(m)$ and $f_{BN}(n)$ for the other blocks should satisfy the following conditions as derived from the signal decoding conditions particular to the equally divided block size ($M_0 \times N_0$) and the signal decoding conditions at a point where blocks of different sizes, especially a block of $M \times N$ size and a block of $M_0 \times N_0$ size, adjoin each other:

$$h_{AM}(m) = f_{AM}(m) = 0 \quad (m < M/2 - M_0/2) \quad \dots (7.a)$$

$$\begin{aligned} h_{AM}(m) f_{AM}(m) \\ = h_{AMO}(m - M/2 + M_0/2) f_{AMO}(m - M/2 + M_0/2) \quad \dots (7.b) \\ (M/2 - M_0/2 \leq m < M/2 + M_0/2) \end{aligned}$$

$$\begin{aligned} h_{AM}(M-1-m) f_{AM}(m) \\ = h_{AMO}(M/2 + M_0/2 - 1 - m) f_{AMO}(m - M/2 + M_0/2) \quad \dots (7.c) \\ (M/2 - M_0/2 \leq m < M/2 + M_0/2) \end{aligned}$$

$$\begin{aligned} h_{AM}(m) f_{AM}(m) = 1 \quad \dots (7.d) \\ (M/2 + M_0/2 \leq m < 3M/2 - M_0/2) \end{aligned}$$

$$\begin{aligned} h_{AM}(m) f_{AM}(m) \\ = h_{AMO}(m - 3M/2 + 3M_0/2) f_{AMO}(m - 3M/2 + 3M_0/2) \quad \dots (7.e) \\ (3M/2 - M_0/2 \leq m < 3M/2 + M_0/2) \end{aligned}$$

$$\begin{aligned} h_{AM}(3M-1-m) f_{AM}(m) \\ = h_{AMO}(3M/2 + 3M_0/2 - 1 - m) f_{AMO}(m - 3M/2 + 3M_0/2) \quad \dots (7.f) \\ (3M/2 - M_0/2 \leq m < 3M/2 + M_0/2) \end{aligned}$$

$$h_{AM}(m) = f_{AM}(m) = 0 \quad (m \geq 3M/2 + M_0/2) \quad \dots (7.g)$$

$$h_{BN}(n) = f_{BN}(n) = 0 \quad (n < N/2 - N_0/2) \quad \dots (7.h)$$

$$h_{BN}(n) f_{BN}(n)$$

$$= h_{BNO}(n - N/2 + N_0/2) f_{BNO}(n - N/2 + N_0/2) \dots (7.i)$$

$$(N/2 - N_0/2 \leq n < N/2 + N_0/2)$$

$$h_{BN}(N-1-n) f_{BN}(n)$$

$$= h_{BNO}(N/2 + N_0/2 - 1 - n) f_{BNO}(n - N/2 + N_0/2) \dots (7.j)$$

$$(N/2 - N_0/2 \leq n < N/2 + N_0/2)$$

$$h_{BN}(n) f_{BN}(n) = 1 \dots (7.k)$$

$$(N/2 + N_0/2 \leq n < 3N/2 - N_0/2)$$

$$h_{BN}(n) f_{BN}(n)$$

$$= h_{BNO}(n - 3N/2 + 3N_0/2) f_{BNO}(n - 3N/2 + 3N_0/2)$$

$$\dots (7.l)$$

$$(3N/2 - N_0/2 \leq n < 3N/2 + N_0/2)$$

$$h_{BN}(3N-1-n) f_{BN}(n)$$

$$= h_{BNO}(3N/2 + 3N_0/2 - 1 - n) f_{BNO}(n - 3N/2 + 3N_0/2)$$

$$\dots (7.m)$$

$$(3N/2 - N_0/2 \leq n < 3N/2 + N_0/2)$$

$$h_{BN}(n) = f_{BN}(n) = 0$$

$$\dots (7.n)$$

$$(n \geq 3N/2 + N_0/2)$$

The above equations are representative of the conditions for an input signal to be reconstructed even when a block of any size adjoins the $M \times N$ block. Among such equations, (7.a) - (7.g) are associated with the window functions $h_{AM}(m)$ and $f_{AM}(m)$ in the horizontal direction while (7.h) - (7.n) are associated with the window functions $h_{BN}(n)$ and $f_{BN}(n)$ in the vertical direction. What each of the equations means will be described hereinafter. Since the equations (7.a) - (7.g) and the equations (7.h) - (7.n) are identical except for the replacement of M and N , let the following description concentrate on (7.a) - (7.g). Fig. 4a plots a waveform representative of sections where the Product $h_{AM}(m) \cdot f_{AM}(m)$ of the window functions $h_{AM}(m)$ and

$f_{AM}(m)$ in the horizontal direction and the equations (7.a) - (7.g) correspond. The equations (7.a) and (7.g) mean that the transformed signals in this region are not effected by the input signals and do not effect the decoded signals in the associated region. The equation (7.d) can be obtained from the equations (7.a), (7.g) and (6.a) with $M_0 = M$. The equations (7.b), (7.c), (7.e) and (7.f) indicate that the product of h_{AM} and f_{AM} is equal to the product at the same position of the window functions h_{AM0} and f_{AM0} associated with the minimum block size $M_0 \times N_0$. Assume that two blocks of the minimum size adjoin an $M \times N$ block. In such a case, the products $h_{AM}(m) \cdot f_{AM}(m)$ of the horizontal window function $h_{AM}(m)$ for coding and horizontal window function $f_{AM}(m)$ for decoding in the individual blocks have a correlation shown in Figs. 4b-4d. As Fig. 4a indicates, $h_{AM}(m)$ and $f_{AM}(m)$ each has more than $(M + 1)$ signal points which are not zero. Since these window functions correspond to a block whose size in the horizontal direction is M , that the block of interest has more than $(M + 1)$ signal points which are not zero means that it overlaps other blocks. The above-description of the equations and window functions are also true with the window functions $h_{BN}(n)$ and $f_{BN}(n)$ associated with a block whose size in the vertical direction is N .

A procedure for producing an inverse-transformed signal $y_p(m, n)$ from a transformed signal $x_p(m, n)$ by use of the window functions $h_{AM}(m)$, $h_{BN}(n)$, $f_{AM}(m)$ and $f_{BN}(n)$ that satisfy the equations (7.a) - (7.n) will be described, taking the region 2 ($M \leq m \leq 2M - 1$, $0 \leq n \leq N - 1$) shown in Fig. 3b as an example. As shown in Fig. 4a, in this particular region, the horizontal window functions have three different conditions, depending on the position in the horizontal direction. Namely, there exist a region ($M \leq m \leq 1/2(3M - M_0) - 1$) derived from the equation (7.d), a region ($1/2(3M - M_0) \leq m \leq 1/2(3M + M_0) - 1$) derived from the equations (7.e) and (7.f), and a region ($1/2(3M + M_0) \leq m \leq 2M - 1$) derived from the equation (7.g). While this region is subdivided into three regions with respect to the position in the vertical direction also, the following description will concentrate on the horizontal direction since the same description applies to both of the horizontal and vertical directions.

To begin with, in the region ($m = M, M+1, \dots, 3M/2 - M_0/2 - 1$; $n = 0, 1, \dots, N-1$) (21, Fig. 3c):

from the equation (7.d)

$$h_{AM}(m) f_{AM}(m) = 1 \quad (8.a)$$

from the equation (7.g)

$$h_{AM}(3M-1-m) f_{AM}(m) = 0 \quad (8.b)$$

Hence, substituting for the equation (5.b)

$$y_p(m, n) = f_{BN}(n) \left\{ x_p(m, n) h_{BN}(n) - x_p(m, N-1-n) h_{BN}(N-1-n) \right\} \dots (9)$$

In the region ($m = 3M/2 - M_0/2, 3M/2 - M_0/2 + 1, \dots, 3M/2 - M_0/2 - 1$; $n = 0, 1, \dots, N-1$) (22, Fig. 3c):

from the equations (7.e) and (6.a)

$$h_{AM}(m) f_{AM}(m) = 1 - h_{AM0}(m - 3M/2 + M_0/2) \times f_{AM0}(m - 3M/2 + M_0/2) \dots (10.a)$$

from the equations (7.f) and (6.b)

$$h_{AM}(3M-1-m) f_{AM}(m) = h_{AM0}(3M/2 + M_0/2 - 1 - m) \times f_{AM0}(m - 3M/2 + M_0/2) \dots (10.b)$$

Therefore,

$$\begin{aligned} y_p(m, n) = & f_{BN}(n) \{ 1 - h_{AM0}(m - 3M/2 + M_0/2) f_{AM0}(m - 3M/2 + M_0/2) \} \\ & \times \{ x_p(m, n) h_{BN}(n) - x_p(m, N-1-n) h_{BN}(N-1-n) \} \\ & + h_{AM0}(3M/2 + M_0/2 - 1 - m) f_{AM0}(m - 3M/2 + M_0/2) f_{BN}(n) \\ & \times \{ x_p(3M-1-m, n) h_{BN}(n) \\ & - x_p(3M-1-m, N-1-n) h_{BN}(N-1-n) \} \dots (11) \end{aligned}$$

Finally, in the region ($m = 3M/2 + M_0/2, 3M/2 + M_0/2 + 1, \dots, 2M-1$; $n = 0, 1, \dots, N-1$) (23, Fig. 3c): from the equation (7.g)

$$h_{AM}(m) f_{AM}(m) = h_{AM}(3M-1-m) f_{AM}(m) = 0$$

Hence,

$$y_p(m, n) = 0 \quad (12)$$

The relationship described with reference to Fig. 4a also holds true for the vertical window functions $h_{BN}(n)$ and $f_{BN}(n)$. Therefore, by applying the conditions of three regions to the vertical window functions $h_{BN}(n)$ and $f_{BN}(n)$ included in the equations (9) and (11) which are representative of inverse-transformed signals, as has been the case with the horizontal direction, it is possible to represent inverse-transformed signals $y_p(m, n)$ of a block of any size by the window functions $h_{AM0}(m)$, $f_{AM0}(m)$, $h_{BN0}(n)$ and $f_{BN0}(n)$ of the block of the minimum size ($M_0 \times N_0$).

Fig. 5a shown a specific condition wherein a block Q of $M_Q \times N_Q$ size and having the same minimum value ($n = N/2$) in the vertical direction as the block P of $M \times N$ size is located at the right-hand side of the block P. The input signal $x_p(0, 0)$ of the block P is the origin of the axes since the input signals of $2M \times 2N$ points are used to code a block of $M \times N$ size, as stated earlier. Likewise, the input signal $x_Q(0, 0)$ of the block Q is $\{(3M - M_Q/2, (N - N_Q/2))\}$. In the following description, a position in the block Q will be represented by coordinates (m', n') relative to the original $\{3M - M_Q/2, (N - N_Q/2)\}$. Hence, $x_p(m, n)$ and $x_Q(m', n')$ are related as follows:

$$x_Q(m', n') = x_p(m' - M_Q/2 + 3M/2, n' - N_Q/2 + N/2) \quad (14)$$

$$(m' = 0, 1, \dots, 2M_Q-1; n' = 0, 1, \dots, 2N_Q-1)$$

In the region of the input signals (inverse-transformed signals) of the block Q, Fig. 5b, inverse-transformed signals $y_Q(m', n')$ in the upper left zone ($m' = 0, 1, \dots, M_Q-1$; $n' = 0, 1, \dots, N_Q-1$), for example, are produced as follows. The inverse-transformed signals of the block Q can be represented by the equations (5.a) - (5.d) if $m, n, M, N, x_p, y_p, h_{AM}, f_{AM}, h_{BN}$ and f_{BN} of the equations are replaced with $m', n', M_Q, N_Q, x_Q, y_Q, h_{AMQ}, f_{AMQ}, h_{BNQ}$ and f_{BNQ} . Therefore, by using the equation (5.a),

$$y_Q(m', n')$$

$$= x_Q(m', n') h_{AMQ}(m') f_{AMQ}(m') h_{BNQ}(n') f_{BNQ}(n')$$

$$- x_Q(M_Q-1-m', n') h_{AMQ}(M_Q-1-m')$$

$$x f_{AMQ}(m') h_{BNQ}(n') f_{BNQ}(n')$$

$$- x_Q(m', N_Q-1-n') h_{AMQ}(m') f_{AMQ}(m')$$

$$x h_{BNQ}(N_Q-1-n') f_{BNQ}(n')$$

$$+ x_Q(M_Q-1-m', N_Q-1-n')$$

$$x h_{AMQ}(M_Q-1-m') f_{AMQ}(m')$$

$$x h_{BNQ}(N_Q-1-n') f_{BNQ}(n') \dots (15)$$

Assume that the window functions h_{AMQ} , f_{AMQ} , h_{BNQ} and f_{BNQ} satisfy equations (7.a') - (7.n') which has m' , n' , M_Q and N_Q in place of m , n , M and N of the equations (7.a) - (7.n). Let the equation (15) be solved with each of three zones 51-53, Fig. 5b, having different window function conditions. First, in the zone 51 ($m' = 0, 1, \dots, M_Q/2 - M_Q/2-1$; $n' = 0, 1, \dots, N_Q-1$), from the equation (7.a')

$$f_{AMQ}(m') = 0 \quad (16)$$

Substituting it for the equation (15)

$$y_Q(m', n') = 0 \quad (17)$$

In the zone 52 ($m' = M_Q/2 - M_Q/2, M_Q/2 - 2M_Q/2+1, \dots, M_Q/2 - M_Q/2-1$; $n' = 0, 1, \dots, N_Q-1$), from the equations (7.b') and (7.c')

$$h_{AMQ}(m') f_{AMQ}(m')$$

$$= h_{AMO}(m' - M_Q/2 + M_Q/2) f_{AMO}(m' - M_Q/2 + M_Q/2)$$

$$\dots (18)$$

$$h_{AMQ}(M_Q-1-m') f_{AMQ}(m')$$

$$= h_{AMO}(M_Q/2 + M_Q/2-1-m') f_{AMO}(m' - M_Q/2 + M_Q/2)$$

$$\dots (19)$$

Again, substituting it for the equation (15)

$$y_Q(m', n')$$

$$\begin{aligned}
 &= h_{AMO}(m' - M_Q/2 + M_O/2) f_{AMO}(m' - M_Q/2 + M_O/2) f_{BNQ}(n') \\
 &\quad \times \{ x_Q(m', n') h_{BNQ}(n') \\
 &\quad - x_Q(m', N_Q-1-n') h_{BNQ}(N_Q-1-n') \} \\
 &\quad - h_{AMO}(M_Q/2 + M_O/2-1-m') f_{AMO}(m' - M_Q/2 + M_O/2) \\
 &\quad \times f_{BNQ}(n') \{ x_Q(M_Q-1-m', n') h_{BNQ}(n') \\
 &\quad - x_Q(M_Q-1-m', N_Q-1-n') h_{BNQ}(N_Q-1-n') \} \\
 &= h_{AMO}(m' - M_Q/2 + M_O/2) f_{AMO}(m' - M_Q/2 + M_O/2) f_{BNQ}(n') \\
 &\quad \times \{ x_P(m' - M_Q/2 + 3M/2, n' - N_Q/2 + N/2) h_{BNQ}(n') \\
 &\quad - x_P(m' - M_Q/2 + 3M/2, N_Q/2 + N/2-1-n') h_{BNQ}(N_Q-1-n') \} \\
 &\quad - h_{AMO}(M_Q/2 + M_O/2-1-m') \\
 &\quad \times f_{AMO}(m' - M_Q/2 + M_O/2) f_{BNQ}(n') \\
 &\quad \times \{ x_P(M_Q/2 + 3M/2-1-m', n' - N_Q/2 + N/2) h_{BNQ}(n') \\
 &\quad - x_P(M_Q/2 + 3M/2-1-m', N_Q/2 + N/2-1-n') \\
 &\quad \times h_{BNQ}(N_Q-1-n') \} \dots (20)
 \end{aligned}$$

In the zone 53 ($m' = M_Q/2 + M_O/2, M_Q/2 + M_O/2+1, \dots, M_Q-1; n' = 0, 1, \dots, N_Q-1$),

$$h_{AMQ}(m') f_{AMQ}(m') = 1,$$

$$h_{AMQ}(M_Q-1-m') f_{AMQ}(m') = 0$$

Therefore

$$y_Q(m', n')$$

$$\begin{aligned}
 &= f_{BNQ}(n') \{ x_Q(m', n') h_{BNQ}(n') \\
 &\quad - x_Q(m', N_Q-1-n') h_{BNQ}(N_Q-1-n') \} \\
 &= f_{BNQ}(n') \{ x_p(m' - M_Q/2 + 3M/2, n' - N_Q/2 + N/2) h_{BNQ}(n') \\
 &\quad - x_p(m' - M_Q/2 + 3M/2, N_Q/2 + N/2-1-n') \\
 &\quad \times h_{BNQ}(N_Q-1-n') \}
 \end{aligned}$$

In the region extending from the upper right portion of the block P to the upper left portion of the block Q ($m = M, M+1, \dots, (3M+M_Q)/2-1$; $n = 0, 1, \dots, \min\{N, (N+N_Q)/2\}-1$), the inverse-transformed signals of the block P and those of the block Q are combined to produce a composite signal $\tilde{x}_{pQ}(m, n)$, as follows. In the zone $m \geq (3M+M_Q)/2$ (56, Fig. 5c), the inverse-transformed signal $y_p(m, n)$ is zero from the equation (13). In the zone $m \leq (3M-M_Q)/2$ (54, Fig. 5c), the inverse-transformed signal $y_Q(m', n')$ is zero from the equation (17). In the zone $N \leq (N+N_Q)/2$, the inverse-transformed signal $y_p(m, n) = y_Q(m', n')$ is zero. To begin with, the composite signal $\tilde{x}_{pQ}(m, n)$ particular to the zone 54 is determined from $y_Q(m', n') = 0$ and the equation (9):

$$\begin{aligned}
 \tilde{x}_{pQ}(m, n) &= y_p(m, n) \\
 &= f_{BN}(n) \{ x_p(m, n) h_{BN}(n) - x_p(m, N-1-n) h_{BN}(N-1-n) \} \\
 &\dots (24)
 \end{aligned}$$

In the zone 55 ($m = (3M-M_Q)/2, (3M-M_Q)/2+1, \dots, (3M+M_Q)/2-1$; $n = 0, 1, \dots, \min\{N, (N+N_Q)/2\}$), from the equations (11) and (20)

$$\begin{aligned}
 \tilde{x}_{pQ}(m, n) &= y_p(m, n) + y_Q(m - 3M/2 + M_Q/2, n + N_Q/2 - N/2) \\
 &= f_{BN}(n) \{ 1 - h_{AMO}(m - 3M/2 + M_Q/2) f_{AMO}(m - 3M/2 + M_Q/2) \} \\
 &\quad \times \{ x_p(m, n) h_{BN}(n) - x_p(m, N-1-n) h_{BN}(N-1-n) \}
 \end{aligned}$$

$$\begin{aligned}
& + h_{AMO}(3M/2 + M_O/2 - 1 - m) f_{AMO}(m - 3M/2 + M_O/2) f_{BN}(n) \\
& \quad \times \{ x_p(3M - 1 - m, n) h_{BN}(n) \\
& \quad \quad - x_p(3M - 1 - m, N - 1 - n) h_{BN}(N - 1 - n) \} \\
& + h_{AMO}(m - 3M/2 + M_O/2) f_{AMO}(m - 3M/2 + M_O/2) \\
& \quad \times f_{BNQ}(n + N_Q/2 - N/2) \\
& \quad \times \{ x_p(m, n) h_{BNQ}(n + N_Q/2 - N/2) \\
& \quad \quad - x_p(m, N - 1 - n) h_{BNQ}(N_Q/2 + N/2 - 1 - n) \} \\
& - h_{AMO}(3M/2 + M_O/2 - 1 - m) f_{AMO}(m - 3M/2 + M_O/2) \\
& \quad \times f_{BNQ}(n + N_Q/2 - N/2) \\
& \quad \times \{ x_p(3M - 1 - m, n) h_{BNQ}(n + N_Q/2 - N/2) \\
& \quad \quad - x_p(3M - 1 - m, N - 1 - n) h_{BNQ}(N_Q/2 + N/2 - 1 - n) \} \\
& = f_{BN}(n) \{ x_p(m, n) h_{BN}(n) - x_p(m, N - 1 - n) h_{BN}(N - 1 - n) \} \\
& + f_{AMO}(m - 3M/2 + M_O/2) \\
& \quad \times \{ x_p(3M - 1 - m, n) h_{AMO}(3M/2 + M_O/2 - 1 - m) \\
& \quad \quad - x_p(m, n) h_{AMO}(m - 3M/2 + M_O/2) \} \\
& \quad \times \{ h_{BN}(n) f_{BN}(n) - h_{BNQ}(n + N_Q/2 - N/2) \\
& \quad \quad \times f_{BNA}(n + N_Q/2 - N/2) \} \\
& + f_{AMO}(m - 3M/2 + M_O/2) \\
& \quad \times \{ x_p(m, N - 1 - n) h_{AMO}(m - 3M/2 + M_O/2) \\
& \quad \quad - x_p(3M - 1 - m, N - 1 - n) h_{AMO}(3M/2 + M_O/2 - 1 - m) \} \\
& \quad \times \{ h_{BN}(N - 1 - n) f_{BN}(n) \\
& \quad \quad - h_{BNQ}(N_Q/2 + N/2 - 1 - n) f_{BNQ}(n + N_Q/2 - N/2) \} \\
& \dots (25)
\end{aligned}$$

In the equation (25), the first term is equal to the composite signal of the zone 54 (equation (24)). In the zone 56 ($m = 3M/2 + M_O/2, 3M/2 + M_O/2 + 1, \dots, 3M/2 + M_O/2 - 1$; $n = 0, 1, \dots, \min \{N, (N + N_Q/2) - 1\}$), since $y_p(m, n)$ is zero as stated earlier,

$$\begin{aligned}
\tilde{x}_{PQ}(m, n) &= y_Q(m-3M/2+M_Q/2, n+N_Q/2-N/2) \\
&= f_{BNQ}(n+N_Q/2-N/2) \{ x_P(m, n) h_{BNQ}(n+N_Q/2-N/2) \\
&\quad - x_P(m, N-1-n) h_{BNQ}(N_Q/2+N/2-1-n) \}
\end{aligned}$$

Regarding $h_{BN}(n)$ and $f_{BN}(n)$, from the equations (7.h) - (7.n) of window functions.

$$h_{BN}(n) \quad f_{BN}(n)$$

$$= \begin{cases} 0 & (n = 0, 1, \dots, N/2-N_Q/2-1) \dots (27.a) \\ h_{BNO}(n-N/2+N_Q/2) f_{BNO}(n-N/2+N_Q/2) & \dots (27.b) \\ (n = N/2-N_Q/2, N/2-N_Q/2+1, \dots, N/2+N_Q/2-1) \\ 1 & (n=N/2+N_Q/2, N/2+N_Q/2+1, \dots, N-1) \dots (27.c) \end{cases}$$

$$h_{BN}(N-1-n) \quad f_{BN}(n)$$

$$= \begin{cases} 0 & (n = 0, 1, \dots, N/2-N_Q/2-1) \dots (28.a) \\ h_{BNO}(N/2+N_Q/2-1-n) f_{BNO}(n-N/2+N_Q/2) & \dots (28.b) \\ (n = N/2-N_Q/2, N/2-N_Q/2+1, \dots, N/2+N_Q/2-1) \\ 0 & (n=N/2+N_Q/2, N/2+N_Q/2+1, \dots, N-1) \dots (28.c) \end{cases}$$

On the other hand, regarding $h_{BNQ}(n+N_Q/2-N/2)$ and $f_{BNQ}(n+N_Q/2-N/2)$,

$$h_{BNQ}(n') \quad f_{BNQ}(n')$$

$$= \begin{cases} 0 & (n' = 0, 1, \dots, (N_Q - N_O)/2 - 1) \quad \dots (29.a) \end{cases}$$

$$h_{BNO} \left(n' - \frac{N_Q}{2} + \frac{N_O}{2} \right) \quad f_{BNO} \left(n' - \frac{N_Q}{2} + \frac{N_O}{2} \right) \quad \dots (29.b)$$

$$(n' = (N_Q - N_O)/2, (N_Q - N_O)/2 + 1, \dots, (N_Q + N_O)/2 - 1)$$

$$= \begin{cases} 1 & (n' = (N_Q + N_O)/2, (N_Q + N_O)/2 + 1, \dots, N_Q - 1) \quad \dots (29.c) \end{cases}$$

$$h_{BNQ}(N-1-n') \quad f_{BNQ}(n')$$

$$= \begin{cases} 0 & (n' = 0, 1, \dots, (N_Q - N_O)/2 - 1) \quad \dots (30.a) \end{cases}$$

$$h_{BNO} \left(\frac{N_Q}{2} + \frac{N_O}{2} - 1 - n' \right) \quad f_{BNO} \left(n' - \frac{N_Q}{2} + \frac{N_O}{2} \right) \quad \dots (30.b)$$

$$(n' = (N_Q - N_O)/2, (N_Q - N_O)/2 + 1, \dots, (N_Q + N_O)/2 - 1)$$

$$= \begin{cases} 0 & (n' = (N_Q + N_O)/2, (N_Q + N_O)/2 + 1, \dots, N_Q - 1) \quad \dots (30.c) \end{cases}$$

Therefore,

$$h_{BNQ}(n + N_Q/2 - N/2) \quad f_{BNQ}(n + N_Q/2 - N/2)$$

$$= \begin{cases} 0 & (n = 0, 1, \dots, N/2 - N_O/2 - 1) \quad \dots (31.a) \end{cases}$$

$$h_{BNO}(n - N/2 + N_O/2) \quad f_{BNO}(n - N/2 + N_O/2) \quad \dots (31.b)$$

$$(n = N/2 - N_O/2, N/2 - N_O/2 + 1, \dots, N/2 + N_O/2 - 1)$$

$$= \begin{cases} 1 & (n = N/2 + N_O/2, N/2 + N_O/2 + 1, \dots, \frac{N}{2} + \frac{N_O}{2} - 1) \end{cases}$$

$$\dots (31.c)$$

$$h_{BNQ}(N_Q/2 + N/2 - 1 - n) f_{BNQ}(n + N_Q/2 - N/2)$$

$$= \begin{cases} 0 & (n = 0, 1, \dots, N/2 - N_Q/2 - 1) & \dots (32.a) \\ h_{BNO}(N/2 + N_Q/2 - 1 - n) f_{BNO}(n - N/2 + N_Q/2) & \dots (32.b) \\ & (n = N/2 - N_Q/2, N/2 - N_Q/2 + 1, \dots, N/2 + N_Q/2 - 1) \\ 0 & (n = N/2 + N_Q/2, N/2 + N_Q/2 + 1, \dots, N/2 + N_Q/2 - 1) & \dots (32.c) \end{cases}$$

From the equations (24), (25) and (26), the composite signal $\tilde{x}_{PQ}(m, n)$ of the blocks P and Q in the zones 54-56, Fig. 5c ($m = M, M+1, \dots, (3M+M_Q)/2-1$; $n = 0, 1, \dots, \min\{N, N/2 + N_Q/2\} - 1$)

$$\tilde{x}_{PQ}(m, n)$$

$$= \begin{cases} 0 & (m = M, M+1, \dots, (3M+M_Q)/2-1; \\ & n = 0, 1, \dots, N/2 - N_Q/2 - 1) & \dots (33.a) \end{cases}$$

$$\begin{aligned} & x_p(m, n) h_{BNO}(n - N/2 + N_Q/2) f_{BNO}(n - N/2 + N_Q/2) \\ & - x_p(m, N-1-n) h_{BNO}(N/2 + N_Q/2 - 1 - n) \\ & \times f_{BNO}(n - N/2 + N_Q/2) & \dots (33.b) \end{aligned}$$

$$\begin{aligned} & (m = M, M+1, \dots, (3M+M_Q)/2-1; \\ & n = N/2 - N_Q/2, N/2 - N_Q/2 + 1, \dots, N/2 + N_Q/2 - 1) \end{aligned}$$

$$\begin{aligned} & x_p(m, n) \quad (m = M, M+1, \dots, (3M+M_Q)/2-1; \\ & n = N/2 + N_Q/2, N/2 + N_Q/2 + 1, \dots, \min(N-1, N/2 + \frac{N_Q}{2} - 1) & \dots (33.c). \end{aligned}$$

While the composite signals \tilde{x}_{PQ} of the blocks P and Q has been shown and described as being produced with the region divided into three zones in the horizontal direction, it is represented with no regard to the horizontal direction, as seen from the equations (33.a) - (33.c). The equations (33.a) - (33.c) do not include the terms that include $x_p(3M-1-m, n)$ and $x_p(3M-1-m, N-1-n)$, i.e., horizontal reverse-order components. Here, $x_p(3M-1-m, n)$ are $x_p(2M-1, n)$, $x_p(2M-2, n)$, ..., $x_p(M, n)$ in the range of $m = M, M+1, \dots, 2M-1$ and are equal to a signal which is opposite in sequence to $x_p(m, n)$ ($m = M, M+1, \dots, 2M-1$) in the horizontal direction. In this sense, $x_p(3M-1-m, n)$ are referred to as horizontal reverse-order components. Likewise, $x_p(3M-1-m, N-1-n)$ are the horizontal reverse-order components of $x_p(m, N-1-n)$ ($m = M, M+1, \dots, 2M-1$) in the range of $m = M, M+1, \dots, 2M-1$. On the other hand, the second term of the equation (33.b) that includes x_p -

(m, N-l-n) is the vertical reverse-order component of $x_p(m, n)$, ($n = (N-N_0)/2, (N-N_0)/2+1, \dots, (N+N_0)/2-1$). Next, as shown in Fig. 6a, assume two blocks R and S which adjoin the blocks P and Q in the opposite direction in contact with $n = N/2$. The blocks R and S are assumed to have sizes $M' \times N'$ and $M_s \times N_s$, respectively. Fig. 6b shows the overlap of the blocks R and S with respect to an input signal. In Fig. 6b, the origin in the horizontal direction is located at the left end of the input signal of the block R for the sake of simplicity. The composite signal \tilde{x}_{RS} of the inverse-transformed signals of the blocks R and S is also attainable by use of the equation used to determine the composite signal \tilde{x}_{PQ} of the blocks P and Q, if m, N, M_Q and N_Q are replaced with m', M', N', M_s and N_s , respectively. As a result, in the zone $m' = M', M'+1, \dots, (3M'+M_s)/2-1$; $n = \max \{ (N-N')/2, (N-N_s)/2 \}, \dots, (N+N')/2-2, (N+N')/2-1$, the composite signal of the blocks R and S is expressed as:

$$\tilde{x}_{RS}(m', n)$$

$$= \begin{cases} x_R(m', n) & \dots (34.a) \end{cases}$$

$$(n = \max \{ (N-N')/2, (N-N_s)/2 \}, \dots, (N-N_0)/2-1:$$

(Fig. 6b, zone 70))

$$x_R(m', n) h_{BNO} \left(n - \frac{N}{2} + \frac{3N_0}{2} \right) f_{BNO} \left(n - \frac{N}{2} + \frac{3N_0}{2} \right)$$

$$+ x_R(m', N'-1-n) h_{BNO} \left(\frac{N}{2} + \frac{3N_0}{2} - 1-n \right)$$

$$f_{BNO} \left(n - \frac{N}{2} + \frac{3N_0}{2} \right) \dots (34.b)$$

$$(n = (N-N_0)/2, (N-N_0)/2+1, \dots, (N+N_0)/2-1:$$

(Fig. 6b, zone 71))

$$0 \dots (34.c)$$

$$(n = (N+N_0)/2, (N+N_0)/2+1, \dots, (N+N')/2-1:$$

(Fig. 6b, zone 72))

As the equations (34.a) - (34.c) indicate, the composite signal \tilde{x}_{RS} does not include horizontal reverse-order components. On the other hand, regarding the equation (34.b), although it includes the term $x_R(m', N'-1-n)$, i.e., the vertical reverse-order components, the vertical reverse-order components are opposite in polarity to those of the composite signal \tilde{x}_{PQ} of the blocks P and Q. Hence, by adding \tilde{x}_{PQ} and \tilde{x}_{RS} , it is possible to cancel the vertical reverse-order components and to thereby reconstruct the input signals $x_p(m, n)$. It is to be noted that the blocks R and S may be located at any positions and have any block sizes so long as they adjoin the blocks P and Q at the opposite side of the line $n = N/2$.

Even when the blocks P and Q adjoin each other on a vertical line, the previously stated equations free the decoded signals associated with the blocks P and Q from vertical reverse-order components. So long as other blocks adjoining the blocks P and Q are located at the opposite side of the above-mentioned vertical line, overlaying the inverse-transformed signals is successful in reconstructing input signals while cancelling the horizontal reverse-order components with no regard to the block size and the position in the vertical direction.

As stated above, the present invention is capable of reconstructing an input signal even when a

bidimensional input signal is split into overlapping rectangular blocks each having a particular size.

Referring to Fig. 7, a coding apparatus and a decoding apparatus embodying the present invention is shown. As shown, the coding apparatus has a dividing circuit 1 and a linear transform circuit 2. The dividing circuit 1 divides a bidimensional input signal into a plurality of rectangular blocks each having a particular size while generating block division information. Assuming that a given block produced by the dividing circuit 1 is $M_p \times N_p$, the linear transform circuit 2 calculates the transformed signals of $M_p \times N_p$ points by linear transform by use of $2M_p \times 2N_p$ sampling points around the block of interest. The block information from the dividing circuit 1 and the transformed signal information from the linear transform circuit 2 are multiplexed by a multiplexer 5 and then sent to the decoding apparatus.

The decoding apparatus has a linear transform circuit 3 and a synthesizing circuit 4 which overlays the signals of blocks. The linear transform circuit 3 calculates inverse-transformed signals of $2M_p \times 2N_p$ points by linear transform on the basis of the transformed signal data and block information which are separated by a demultiplexer 6. The synthesizing circuit 4 arranges the inverse-transformed signals on the basis of the block information and, in portions where blocks overlap, adds the signals to produce a decoded signal.

Figs. 8a and 8b each shows a specific manner of dividing a bidimensional input signal into blocks. In Fig. 8a, a bidimensional signal is repetitively bisected in the horizontal and vertical directions a necessary number of times. In Fig. 8b, such a signal is divided into rectangular blocks each having a particular size. Regarding the basis of division, some different approaches are available, e.g., calculating the amounts of code of transformed signals and selecting particular division which makes the amounts of code minimum, and selecting division which makes the degree of power concentration on transformed signals representative of the intensities of lower frequency components maximum.

Figs. 9a and 9b each shows a specific construction of circuitry for selecting a dividing method. In Fig. 9a, the circuitry has n dividing circuits 911-91n, n encoders or linear transform circuits 921-92n, n coded signal amount calculators 931-93n, and a single decision circuit 941. The dividing circuits 911-91n each divides a bidimensional input signal by a particular dividing method. The encoders 921-92n each produces transformed signals with each of the blocks produced by the associated dividing circuit. The coded signal amount calculators 931-93n each calculates the amount of code of the transformed signal fed thereto from the associated encoder. The outputs of the coded signal amount calculators 931-93n are connected to the decision circuit 941. The decision circuit 941 compares one frame of bidimensional input signals derived from the respective dividing methods and outputs a selection signal indicative of one of the dividing methods which is smaller in the amount of coded signals than the others. The circuitry shown in Fig. 9b is identical with the circuitry of Fig. 9a except that the coded signal amount calculators 931-93n are replaced with power intensity calculators 951-95n. The circuitry of Fig. 9b selects one of the dividing methods which makes the power intensity on the lower frequencies of transformed signals maximum. The dividing circuit 1 shown in Fig. 7 divides a bidimensional input signal into a plurality of blocks by using a particular method which is selected by the circuitry of Fig. 9a or 9b beforehand. The block information associated with each block, e.g., a block size and a start point are coded, multiplexed with transformed signals and then sent to the decoding side since they are essential in the event of decoding. The division shown in Fig. 8a, for example, may be implemented with the coding method described in the previously stated Chen's paper. Specifically, as Chen shows in Fig. 2 of the paper, "1" is assigned to a given block when the block is divided into four subblocks, or "0" is assigned when it is not divided. When the block is divided into four subblocks, "1" and "0" are selectively and sequentially assigned to the upper left, upper right, lower left and lower right subblocks. As a result, a bit sequence representative of the levels and division states of all of the blocks is produced. This bit sequence is used as block information. Since the size of a given block can be determined on the basis of the level of the block if the size per frame is known, block size information does not have to be sent. In the case of the division shown in Fig. 8b, the reference point of each block such as the coordinates of the upper left point and the block size may be used as block information.

The linear transform circuit 2, Fig. 7, may be implemented by $M_p \times N_p$ multiplying and adding circuits which, assuming that the predetermined maximum block size is $M_p \times N_p$, weight and add the input signals of $2M_p \times 2N_p$ points. Fig. 10a shows a specific construction of the linear transform circuit 2. As shown, the linear transform circuit 2 has $M_p \times N_p$ multiplying and adding circuits 10(l) - 10($M_p \times N_p$) connected to an input signal line in parallel with each other, a block information decoder 15 for decoding block information to produce the size of a block of interest and the selection signal, and a selector 16 for selecting the output signal of one of the multiplying and adding circuits 10(l) - 10($M_p \times N_p$) in response to the selection signal. A window function generating circuit 17 generates window functions $h_{AM}(m)$ and $h_{BN}(n)$ on a direction and block size basis according to the block size and input signal coordinates. A first and a second multiplier 18 and 19 multiply the input signal $x_p(m, n)$ by the window functions $h_{AM}(m)$ and $h_{BN}(n)$, respectively. The multiplying and adding circuits 10(l) - 10($M_p \times N_p$) each has a coefficient memory 12, a multiplier 11, an

accumulator 13, and an address generating circuit 14. Transform operations are as follows. Assume that a block of interest has the size $M_p \times N_p$, i.e., the maximum block size. To transform such a block having the maximum block size, input signals at $2M_p \times 2N_p$ points centering around the block are transformed, as stated previously. As the equation (3) indicates, the transformed signal $X_p(m', n')$ ($0 \leq m' \leq M_p-1$, $0 \leq n' \leq N_p-1$) of the input signal $x_p(m, n)$ ($0 \leq m \leq 2M_p-1$, $0 \leq n \leq 2N_p-1$) is expressed as:

$$\begin{aligned}
 X_p(m', n') = & \sum_{m=0}^{2M_p-1} \sum_{n=0}^{2N_p-1} x_p(m, n) h_{AM}(m) h_{BN}(n) \\
 & \times \cos \left\{ (2m + M_p + 1) (2m' + 1) \pi / (4M_p) \right\} \\
 & \times \cos \left\{ (2n + N_p + 1) (2n' + 1) \pi / (4N_p) \right\} \\
 & \dots (3')
 \end{aligned}$$

In the above transform equation, $h_{AM}(m)$ and $h_{BN}(n)$ are multiplied by the input signal $x_p(m, n)$ by the multipliers 18 and 19, respectively. Hence, if the coefficients of second term and successive terms of the right member are stored in the coefficients memories 12(l) - 12($M_p \times N_p$) of the multiplying and adding circuits 10(l) - 10($M_p \times N_p$), transformed signals are attainable. For example, the coefficient memory 12(l) of the circuit 10(l) stores a coefficient matrix (assumed to be $C_l(m, n)$) made up of $2M_p \times 2N_p$ elements resulted from $\cos \{(2m + M_p + 1) \pi / (4M_p)\} \times \cos \{(2n + N_p + 1) \pi / (4N_p)\}$ ($0 \leq m \leq 2M_p-1$, $0 \leq n \leq 2N_p-1$) as transform coefficients for blocks of the maximum size. Likewise, the coefficient memory 12($M_p \times N_p$) of the " $M_p \times N_p$ " multiplying and adding circuit 10($M_p \times N_p$) stores a coefficient matrix of $2M_p \times 2N_p$ elements resulted from $\cos \{(2m + M_p + 1) (2M_p-1) \pi / (4M_p)\} \times \cos \{(2n + N_p + 1) (2N_p-1) \pi / (4N_p)\}$. It will be seen from the equation (3') that the coefficient changes with the block size. Hence, a plurality of coefficient matrixes each being assigned to a particular block size are stored in each coefficient memory. Which of such coefficient matrixes should be selected is determined on the basis of block information which the block information decoder 15 produces. Implemented by ROMs, for example, the address generating circuits 14(l) - 14($M_p \times N_p$) each generates, in response to block size information from the block information decoder 15, the leading address of associated one of the coefficient memories 12(l) - 12($M_p \times N_p$) where a coefficient matrix matching the block size of interest is stored. A reading circuit, not shown, sequentially reads in synchronism with the input signals the transform coefficients $C_l(m, n)$ associated with the block of maximum size out of the leading address having been designated by the address generating circuit 14(l). The multiplier 11(l) multiplies the window function-multiplied input signals $x_p(m, n) h_{AM}(m) \cdot h_{BN}(n)$ by the transform coefficients $C_l(m, n)$. The accumulator 13(l) accumulates the results of multiplications to produce a transformed signal $X_p(0, 0)$. The other multiplying and adding circuits also perform similar operations in parallel. As a result, transformed signals $X_p(0, 0) - X_p(M_p-1, N_p-1)$ are obtained. The transformed signals $X_p(m', n')$ are sequentially selected by the selector 16 and fed therefrom to the multiplexer 5. The selection by the selector 16 is controlled by the selection signal which is generated by the block information decoder 15. Specifically, the block information decoder 15 has, for example, a table listing the correspondence of the input signals of the selector 16 and the transformed signals $X_p(m', n')$ and controls the selector 16 in such a manner as to select only necessary ones of decoded block information. In the case of a block of maximum size, the decoder 15 generates a selection signal such that the selector 16 sequentially selects the output signals of all of the multiplying and adding circuits, the output signal $X_p(0, 0)$ of the circuit 10(l) being first, for example. The resulted transformed signals $x_p(m', n')$ are multiplexed with corresponding block information by the multiplexer 5, Fig. 7, and then transmitted to the decoding side. The window function generating circuit 17 will be described more specifically hereinafter. The present invention assigns different window functions on a direction and block size basis. Moreover, the values of window functions change from one region to another of the input signal, as described above. For this reason, the window function generating circuit 17 of the linear transform circuit 2 shown in Fig. 10a and located at the coding size generates window functions $h_{AM}(m)$ and $h_{BN}(n)$ in response to the input signal and block information. Fig. 11a shows a specific construction of the window function generating circuit 17. In Fig. 11a, the input signals $x_p(m, n)$ and the block information are applied to a data counter 51 and a separator 31, respectively. The data counter 51 counts the input signals $x_p(m, n)$ to produce the coordinates m and n of the input

signals. The coordinate m in the horizontal direction and the coordinate n in the vertical direction are fed to a first region decision circuit 32 and a second region decision circuit 42, respectively. The separator 31 separates the block information from the block information decoder 15 into a horizontal block size M and a vertical block size N and transfers the resulted block sizes M and N to the decision circuits 32 and 42, respectively. Since the two region decision circuits 32 and 42 are identical in construction and operation, let the following description concentrate on the region decision circuit 32 by way of example. The region decision circuit 32 has n threshold generating circuits 321-32n, n comparators 331-33n, and a counter 34. On receiving the horizontal block size M , the threshold generating circuits 321-32n generate corresponding n threshold values. The comparators 331-33n compare the horizontal coordinate m of the input signal with the n threshold values. The counter 34 receives the outputs of all of the comparators 331-33n to determine the number of comparators that have outputted a coincident output. More specifically, the threshold generating circuits 321-32n determine, on the basis of the horizontal block size M , the value of horizontal coordinate m where the value of the window function $h_{AM}(m)$ corresponding to the block size changes. When the window functions are configured as shown in Figs. 4a - 4c, four threshold values, i.e., $(M/2 - M_0/2)$, $(M/2 + M_0/2)$, $(3M/2 - M_0/2)$ and $(3M/2 + M_0/2)$ are needed. Since the minimum block size in the horizontal direction is determined beforehand, the threshold values can be generated if the horizontal block size M is given. The number of threshold generating circuits may be determined in matching relation to the window function $h_{AM}(m)$ whose maximum number changes. Corresponding one-to-one to the threshold generating circuits 321-32n, the comparators 331-33n compare the horizontal coordinate of the input signal from the data counter 51 with the threshold values from their associated threshold generating circuits 321-32n and generate a pulse when they are coincident. The output pulses of the comparators 331-33n are applied to the count terminals of the counter 34, so that the counter 34 outputs the number of input pulses. The number of pulses is indicative of a particular region to which the horizontal coordinate m of the input signal belongs. Fig. 12 tabulates a relation between the horizontal coordinate m of the input signal and the output or the counter 34. In Fig. 12, the counter 34 is assumed to be a 4-bit counter. A ROM 35 stores window functions $h_{AM}(m)$ assigned to the horizontal direction. The lower bits and the upper bits of the address terminal of the ROM 35 are allocated to the output of the counter 34 and the horizontal block size M , respectively. Consequently, a particular window function matching a block size is selected with the result that the value of the window function is changed in association with the horizontal coordinate of the input signal. Fig. 13 shows a specific relation between the address of the ROM 35 and the content thereof on the assumption that the window function $h_{AM}(m)$ has five different values depending on the horizontal coordinate of the input signal. Likewise, the second region decision circuit 42 produces a window function $h_{BN}(n)$ matching a vertical block size of interest from a ROM 45 on the basis of the vertical block size N and the vertical coordinate n of the input signal. A counter 44 and the ROM 45 are respectively identical with the counter 34 and the ROM 35 except for the replacement of m , M , M_0 and $h_{AM}(m)$ with n , N , N_0 and $h_{BN}(n)$.

Referring again to Fig. 7, the block information and transformed (coded) signal multiplexed by the multiplexer 5 and then transmitted are separated by the demultiplexer 6. The transformed signal $X_p(m', n')$ and the block information are applied to the linear transform circuit 3 and the synthesizing circuit 4, respectively. Fig. 10b shows a specific construction of the linear transform circuit 3 which is substantially identical with the construction of Fig. 10a except for the number of multiplying and adding circuits $20(l) - 20(2M_p \times 2N_p)$. As the equation (4) indicates, an inverse-transformed signal $y(m, n)$ ($0 \leq m \leq 2M_p - 1$, $0 \leq n \leq 2N_p - 1$) derived from a transformed signal $X(m', n')$ ($0 \leq m' \leq M_p - 1$, $0 \leq n' \leq N_p - 1$) is expressed as:

$$\begin{aligned}
 y_p(m, n) = & \sum_{m'=0}^{M_p-1} \sum_{n'=0}^{N_p-1} X(m', n') f_{AM}(m) f_{BN}(n) / (M_p N_p) \\
 & \times \cos \left\{ (2m + M_p + 1) (2m' + 1) \pi / (4M_p) \right\} \\
 & \times \cos \left\{ (2n + N_p + 1) (2n' + 1) \pi / (4N_p) \right\} \\
 & \dots (4')
 \end{aligned}$$

A first and a second multiplier 28 and 29 multiply respectively the transformed signal $x(m', n')$ by $f_{AM}(m)$ and $f_{BN}(n)/(MN)$ included in the above inverse transform equation. Coefficient memories 22(l) - 22($2M_p \times 2N_p$)

2N_p) included in the multiplying and adding circuits 20(l) - 20(2M_p x 2N_p), respectively, store the coefficients represented by the second term and successive terms of the right member. The coefficient memory 22(l), for example, stores inverse-transform coefficients associated with the block of maximum size which are an inverse-transform coefficient matrix (assumed to be C_i⁻¹(m', n')) of M_p x N_p elements represented by cos {-(M_p+l) (2m'+l) π/(4M_p)} x cos {(N_p+l) (2n'+l) π/(4N_p)} (0 ≤ m' ≤ M_p-l, 0 ≤ n' ≤ N_p-l). Likewise, the elements constituting the inverse-transform coefficient matrix

$$C_{2M_p \times 2N_p}^{-1}$$

(m', n') stored in the "2M_p x 2N_p" coefficient memory 22(2M_p x 2N_p) and associated with the block of maximum size are represented by cos {(5M_p-l) (2m'+l) π/(4M_p)} x cos {(5N_p-l) (2n'+l) π/(4N_p)} (0 ≤ m' ≤ M_p-l, 0 ≤ n' ≤ N_p-l). The inverse-transform coefficients, like the transform coefficients, change with the block size. Hence, the coefficient memories each stores a plurality of inverse-transform coefficient matrixes matching different block sizes. A block information decoder 25 and address generating circuits 24(l) - 24-(2M_px2N_p) included in the multiplying and adding circuits cooperate to cause particular inverse-transform coefficient matrixes to be selected in matching relation to the block size. The selection principle is the same as with the coding linear transform circuit 2 and will not be described for simplicity. The inverse-transform coefficient matrix

$$C_1^{-1}$$

(m', n') selected out of the coefficient memory 22(l) by the address generating circuit 24(l) is sequentially read out in synchronism with the transform signals X_p (m', n') under the control of a reading circuit, not shown, and multiplied by the multiplier 21(l). An accumulator 23(l) accumulates the results of multiplications to produce y_p (0, 0). The other multiplying and summing circuits also perform such operations in parallel. As a result, y_p (0, 0), y_p (0, l), ..., y_p (2M_p-l 2N_p-l) are obtained. A selector 26 sequentially selects the inverse-transformed signals y_p (m, n) in response to selection signals from a block information decoder 25 and transfers them to the synthesizing circuit 4. Fig. 11b shows a specific construction of the window function generating circuit 27 shown in Fig. 10b. The circuitry shown in Fig. 11b is identical in construction and operation with the circuitry of Fig. 11a except for a multiplier 66 and a divider 67, and redundant description will be avoided for simplicity. The multiplier 66 and divider 67 are used to perform the division of the right member by the product of block sizes MN which occurs only in the inverse-transform equation (4) or (4'). Specifically, the multiplier 66 produces the product MN of horizontal and vertical block sizes M and N which a separator 61 outputs, while the divider 67 divides a vertical window function f_{BN}(n) for decoding read out of a ROM 75 by the product MN, i.e., it produces f_{BN}(n)/MN. While the specific construction of Fig. 11b divides the vertical window function f_{BN}(n) by the product MN of block sizes, it may be modified to divide the horizontal window function f_{AM}(n) or the division may be effected by any other section of the linear transform circuit 3, if the inverse-transform equation (4') can be executed.

Referring to Fig. 14, a specific construction of the synthesizing circuit 4 is shown. As shown, the circuit 4 has a block information decoder 91, a memory 93 whose capacity is great enough to accommodate one frame of bidimensional input signals, and an address generating circuit 92. The block information decoder 91 outputs block origin information in response to block information fed thereto from the demultiplexer 6. The address generating circuit 92 generates an address signal meant for the memory 93 in response to an inverse-transformed signal and block information. An adder 95 adds signal data read out of the memory 93 and being decoded and a inverse-transformed signal. A decoding controller 94 controls the reading and writing of data in the memory 93 while decoding is under way. A reading controller 96 controls the read-out of one frame of decoded signals associated with bidimensional input signals. In operation, the block information decoder 91 produces, in response to block information separated by the demultiplexer 6, Fig. 7, a transform-coded block size and the position of the origin of the block (assumed to be the upper left point) in an input signal frame (block origin coordinates). The address generating circuit 92 generates a particular address of the memory 93 for writing a transformed signal on the basis block origin coordinates and block size. The memory 93 has an address space identical with the sampling points of one frame of bidimensional input signals and is used for temporary storage during the course of decoding operation. On receiving the inverse-transformed signal y(m,n), the decoding controller 94 reads corresponding signal data

being decoded out of the memory 93 within one sampling time of the inverse-transformed signal. Then, the decoding controller 94 writes in the same address the sum of the signal data and inverse-transformed signal which the adder 95 produces. As a result, inverse-transformed signals in a region where k blocks overlap are added k times. When the frame of input signals changes, the decoding controller 94 generates an end-of-decode signal to inform the reading controller 96 of the end of decoding. To detect the change of frame, a frame pulse usually included in a picture signal may be detected. In response to the end-of-decode signal, the reading controller 96 reads data out of the leading address of the memory 93 and sequentially outputs them as a decoded signal. On reading out all the data, the reading controller 96 resets the contents of the memory 93.

By the operations described above, the present invention produces correct decoded signals even when a bidimensional input signal is divided into blocks each having a particular block size and overlapping the others. Among the window functions assigned to the linear transform circuits 2 and 3, the window functions $h_{AMO}(m)$, $h_{BNO}(n)$, $f_{AMO}(m)$ and $f_{BNO}(n)$ for the block of the minimum size may be implemented as the window function represented by the equation 9 of the previously stated H. Schiller's paper or the window function shown in table 1 of J.P. Princen et al "Analysis/Synthesis Filter Bank Design Based on Time Domain Aliasing Cancellation", IEEE Transactions on Acoustics, Speech, and Signal Processing. Vol. 34, No. 5, October 1986, pp. 1153-1161. It is to be noted that $h_{AMO}(m)$ and $h_{BNO}(n)$ may have an identical configuration when the block of interest is square, i.e., $M_0 = N_0$ so long as they satisfy the equations (6.a) - (6.d). Regarding the window functions $h_{AM}(m)$, $h_{BN}(n)$, $f_{AM}(m)$ and $f_{BN}(n)$ for block sizes other than the minimum block size, assuming in the equations (7.a) - (7.n)

$$\begin{cases} h_{AM}(m) = \alpha_{AM} h_{AMO}(m) \\ f_{AM}(m) = 1/\alpha_{AM} f_{AMO}(m) \end{cases}$$

$$\begin{cases} h_{BN}(n) = \alpha_{BN} h_{BNO}(n) \\ f_{BN}(n) = 1/\alpha_{BN} f_{BNO}(n) \end{cases}$$

then

$h_{AM}(m)$

$$= \begin{cases} 0 & (m < M/2 - M_0/2) \\ \alpha_{AM} h_{AMO} (m - M/2 + M_0/2) & (M/2 - M_0/2 \leq m < M/2 + M_0/2) \\ \alpha_{AM} & (M/2 + M_0/2 \leq m < 3M/2 - M_0/2) \\ \alpha_{AM} h_{AMO} (m - 3M/2 + 3M_0/2) & (3M/2 - M_0/2 \leq m < 3M/2 + M_0/2) \\ 0 & (m \geq 3M/2 + M_0/2) \end{cases}$$

 $h_{BN}(n)$

$$= \begin{cases} 0 & (n < N/2 - N_0/2) \\ \alpha_{BN} h_{BNO} (n - N/2 + N_0/2) & (N/2 - N_0/2 \leq n < N/2 + N_0/2) \\ \alpha_{BN} & (N/2 + N_0/2 \leq n < 3N/2 - N_0/2) \\ \alpha_{BN} h_{BNO} (n - 3N/2 + 3N_0/2) & (3N/2 - N_0/2 \leq n < 3N/2 + N_0/2) \\ 0 & (n \geq 3N/2 + N_0/2) \end{cases}$$

$$f_{AM}(m)$$

$$= \begin{cases} 0 & (m < M/2 - M_0/2) \\ f_{AMO}(m - M/2 + M_0/2) / \alpha_{AM} & (M/2 - M_0/2 \leq m < M/2 + M_0/2) \\ 1 / \alpha_{AM} & (M/2 + M_0/2 \leq m < 3M/2 - M_0/2) \\ f_{AMO}(m - 3M/2 + 3M_0/2) / \alpha_{AM} & (3M/2 - M_0/2 \leq m < 3M/2 + M_0/2) \\ 0 & (m \geq 3M/2 + M_0/2) \end{cases}$$

$$f_{BN}(n)$$

$$= \begin{cases} 0 & (n < N/2 - N_0/2) \\ f_{BNO}(n - N/2 + N_0/2) / \alpha_{BN} & (N/2 - N_0/2 \leq n < N/2 + N_0/2) \\ 1 / \alpha_{BN} & (N/2 + N_0/2 \leq n < 3N/2 - N_0/2) \\ f_{BNO}(n - 3N/2 + 3N_0/2) / \alpha_{BN} & (3N/2 - N_0/2 \leq n < 3N/2 + N_0/2) \\ 0 & (n \geq 3N/2 + N_0/2) \end{cases}$$

This is successful in reconstructing, when a transformed signal with no artifacts is decoded, an input signal in the decoded signal. In the above window functions, α_{AM} and α_{BN} are constants. Specifically, assuming that $M = N = 16$ and $M_0 = N_0 = 8$, the window functions may be selected as:

$$h_{AMO}(m) = f_{AMO}(m) = h_{BNO}(m) = f_{BNO}(m)$$

$$= \begin{cases} \sin \left\{ \frac{\pi}{16} \left(m + \frac{1}{2} \right) \right\} & (m = 0, 1, \dots, 15) \\ 0 & (m < 0 \text{ or } 16 < m) \end{cases}$$

$$h_{AM}(m) = f_{AM}(m) = h_{BN}(m) = f_{BN}(m)$$

$$= \begin{cases} 0 & (m < 4) \\ \sin \left\{ \frac{\pi}{16} \left(m - \frac{7}{2} \right) \right\} & (m = 4, 5, \dots, 11) \\ 1 & (m = 12, 13, \dots, 19) \\ \sin \left\{ \frac{\pi}{16} \left(m - \frac{23}{2} \right) \right\} & (m = 20, 21, \dots, 27) \\ 0 & (m \geq 28) \end{cases}$$

In a modified form of the present invention, the window functions may be changed in matching relation to the size of the block adjoining the block of interest. Assuming $M = N = 16$ and $M_0 = N_0 = 8$ as mentioned above, when a block of 16×16 size adjoins a block of interest whose size is 16×16 at the right-hand side of the latter in the horizontal direction, the right half ($m \geq 16$) of the window functions $h_{AM}(m)$ and $f_{AM}(m)$ may be

$$\begin{cases} \sin \left\{ \frac{\pi}{32} \left(m + \frac{1}{2} \right) \right\} & (m = 16, 17, \dots, 31) \\ 0 & (m \geq 32) \end{cases}$$

The same holds true for the block adjoining the block of interest at the left-hand side of the latter and the top or the bottom of the block of interest. When blocks neighboring the block of interest are of 16×16 size,

$$\begin{cases} 0 & (m < 0) \\ \sin \left\{ \frac{\pi}{32} \left(m + \frac{1}{2} \right) \right\} & (m = 0, 1, \dots, 31) \\ 0 & (m \geq 32) \end{cases}$$

In summary, the present invention produces a decoded signal by adding the inverse-transformed signals of nearby blocks. This not only reduces block boundary artifacts but also allows the block size to be changed in matching relation to the characteristic of a signal. The present invention, therefore, achieves extremely efficient coding by selecting a comparatively large block size in, for example, an area occupied by the same pattern or a comparatively small block size in, for example, an area where the characteristic of the signal sharply changes. Another advantage attainable with the present invention is that the number of sampling points of a signal to be transmitted is the same as that of an input signal.

Claims

1. A method of coding and decoding a bidimensional signal, comprising the steps of:
at a coding side

- (a) dividing a bidimensional input signal into rectangular blocks of a plurality of sizes;
(b) providing window functions $h_{AM}(m)$ and $h_{BN}(n)$ on a direction and block size basis;
(c) calculating, assuming that a block of interest has an $M \times N$ size, transformed signals $X_p(i, i)$ from input signals $x_p(m, n)$ ($m = 0, 1, \dots, 2M-1$; $n = 0, 1, \dots, 2N-1$) by a transform equation:

$$X_p(k, i) = \sum_{m=0}^{2M-1} \sum_{n=0}^{2N-1} x_p(m, n) h_{AM}(m) h_{BN}(n) \\ \times \cos \left\{ (2m + M + 1) (2k + 1) \pi / (4M) \right\} \\ \times \cos \left\{ (2n + N + 1) (2i + 1) \pi / (4N) \right\} \\ (k = 0, 1, \dots, M-1; i = 0, 1, \dots, N-1)$$

- (d) transmitting block division information and said transformed signals to a decoding side;
at said decoding side

- (e) providing window functions $f_{AM}(m)$ and $f_{BN}(n)$ corresponding to said window functions $h_{AM}(m)$ and $h_{BN}(n)$, respectively;
(f) calculating, when a block size of said transformed signals sent from said coding side is $M \times N$, inverse-transformed signals $y_p(m, n)$ of $2M \times 2N$ points from said transformed signals by using a transform equation:

$$y_p(m, n) = \sum_{k=0}^{M-1} \sum_{i=0}^{N-1} X_p(k, i) f_{AM}(m) f_{BN}(n) / (MN) \\ \times \cos \left\{ (2m + M + 1) (2k + 1) \pi / (4M) \right\} \\ \times \cos \left\{ (2n + N + 1) (2i + 1) \pi / (4N) \right\} \\ (m = 0, 1, \dots, 2M-1; n = 0, 1, \dots, 2N-1)$$

- (g) arranging said inverse-transformed signals according to said block division information sent from said coding side; and
(h) adding, in a portion where a plurality of blocks overlap, said inverse-transformed signals to produce a decoded signal.

2. An apparatus for coding a bidimensional signal, comprising:
means for dividing a bidimensional input signal into rectangular blocks of a plurality of sizes; and

means having window functions $h_{AM}(m)$ and $h_{BN}(n)$ on a block size and direction basis for calculating, when a block of interest has an $M \times N$ size, transformed signals $X_p(k, i)$ of $M \times N$ points from input signals $x_p(m, n)$ ($m = 0, 1, \dots, 2M-1$; $n = 0, 1, \dots, 2N-1$) of $2M \times 2N$ points centering around said block of interest by using a transform equation:

$$X_p(k, i) = \sum_{m=0}^{2M-1} \sum_{n=0}^{2N-1} x_p(m, n) h_{AM}(m) h_{BN}(n) \times \cos \left\{ (2m + M + 1) (2k + 1) \pi / (4M) \right\} \times \cos \left\{ (2n + N + 1) (2i + 1) \pi / (4N) \right\}$$

$$(k = 0, 1, \dots, M-1; i = 0, 1, \dots, N-1)$$

said block division information and said transformed signals being outputted.

3. An apparatus for decoding a bidimensional signal, comprising:

means having window functions $f_{AM}(m)$ and $f_{BN}(n)$ corresponding respectively to window functions $h_{AM}(m)$ and $h_{BN}(n)$ recited in claim 2 for calculating, when transformed signals transmitted from a coding side have a block size of $M \times N$, inverse-transformed signals $y_p(m, n)$ of $2M \times 2N$ points from input transformed signals by using a transform equation:

$$y_p(m, n) = \sum_{k=0}^{M-1} \sum_{i=0}^{N-1} X_p(k, i) f_{AM}(m) f_{BN}(n) / (MN) \times \cos \left\{ (2m + M + 1) (2k + 1) \pi / (4M) \right\} \times \cos \left\{ (2n + N + 1) (2i + 1) \pi / (4N) \right\}$$

$$(m = 0, 1, \dots, 2M-1; n = 0, 1, \dots, 2N-1)$$

means for arranging said inverse-transformed signals according to block division information transmitted from said coding side and, in a portion where a plurality of blocks overlap, adding said inverse-transformed signals to produce a decoded signal.

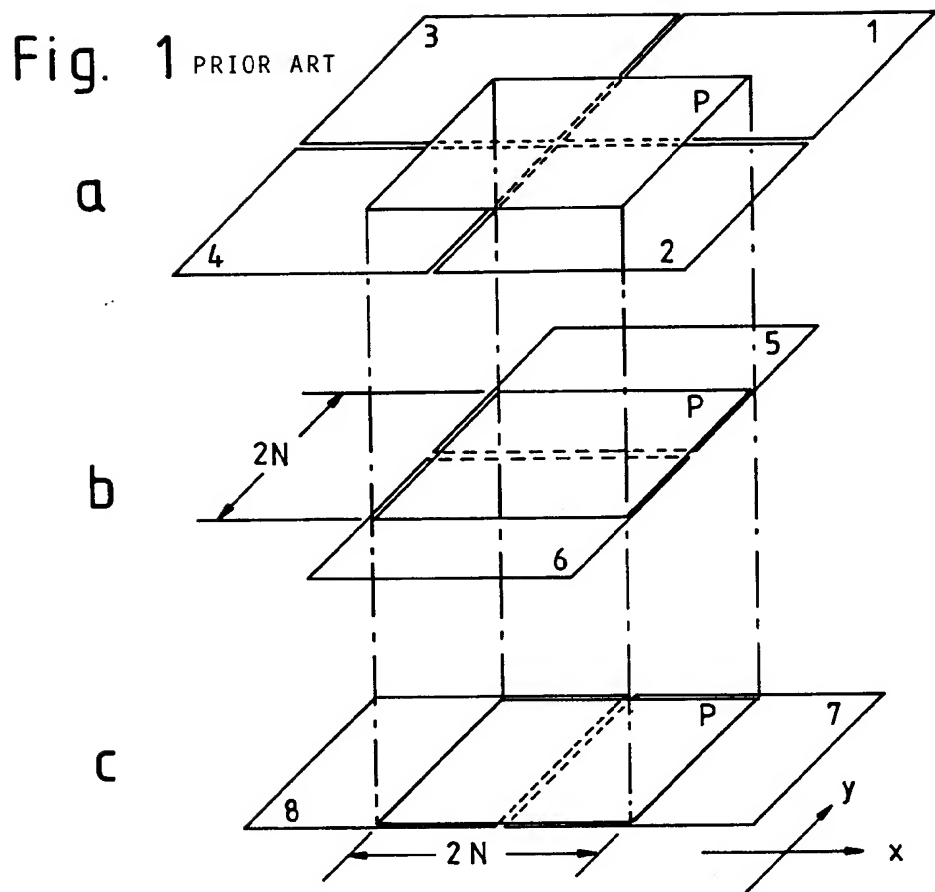


Fig. 2

PRIOR ART

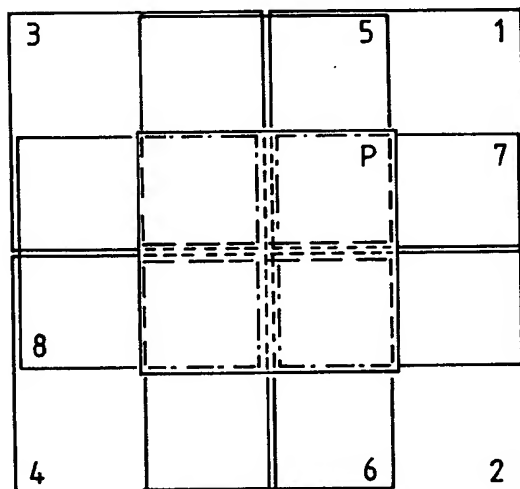


Fig. 3

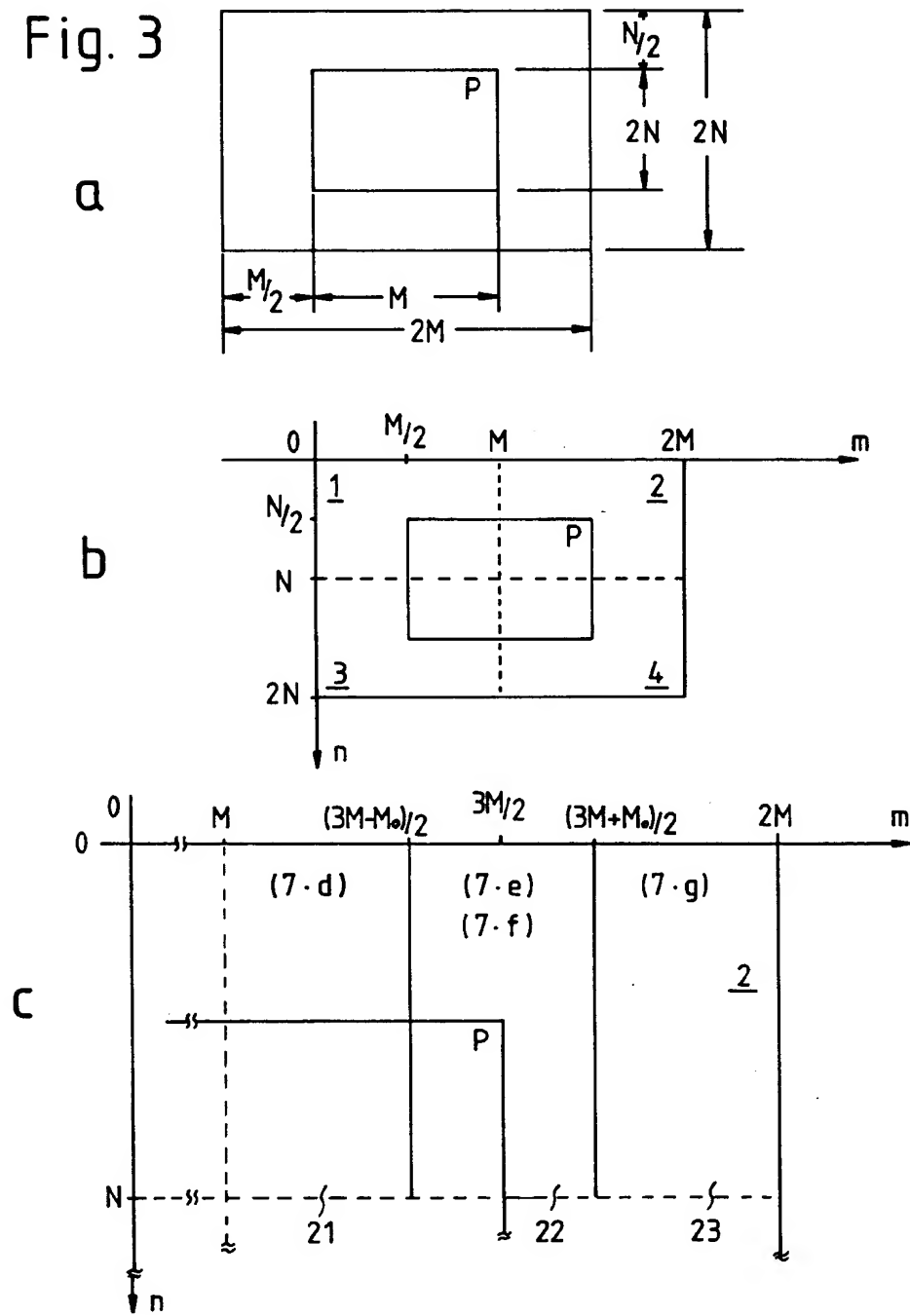
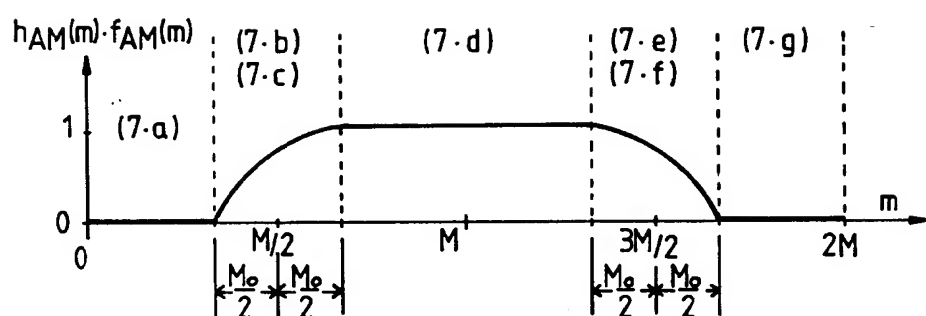
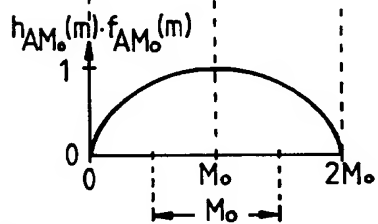
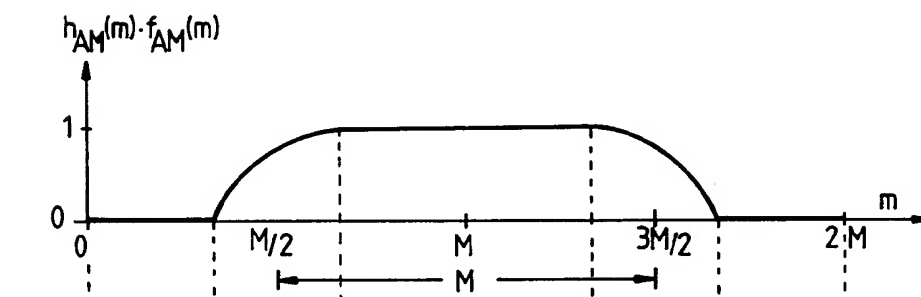


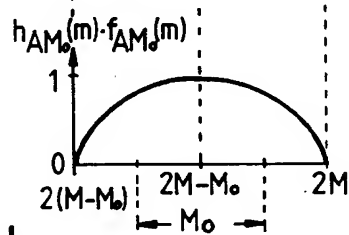
Fig. 4 a



b



c



d

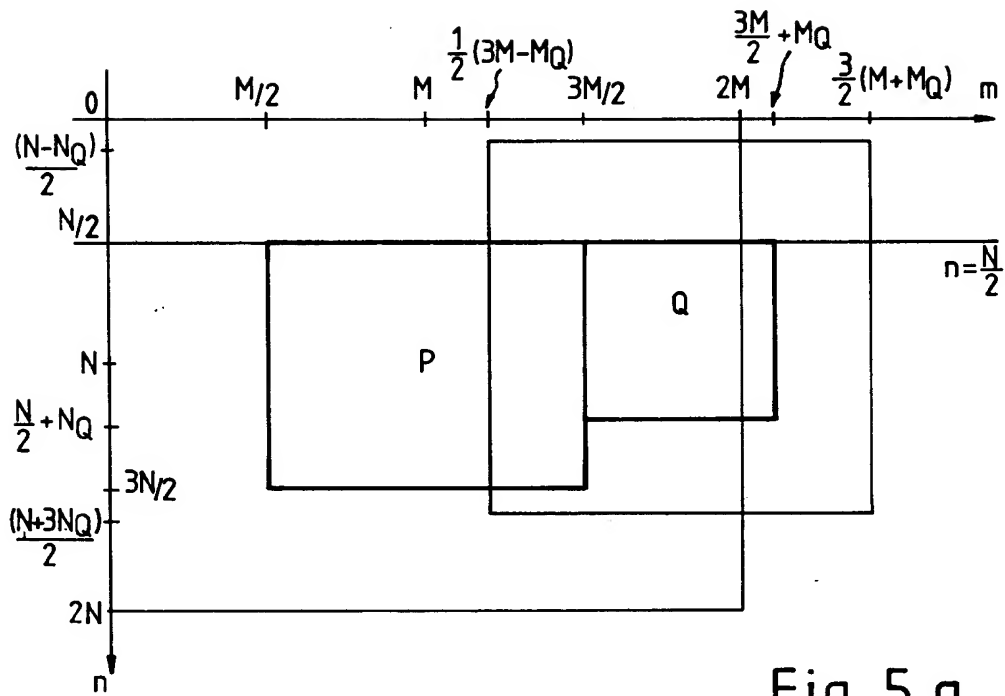
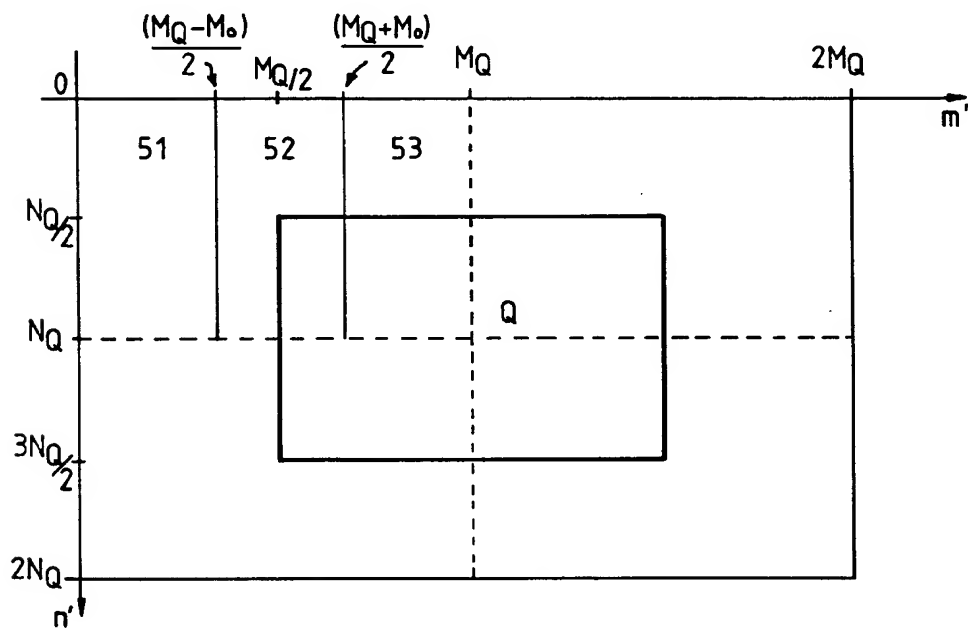


Fig. 5 b



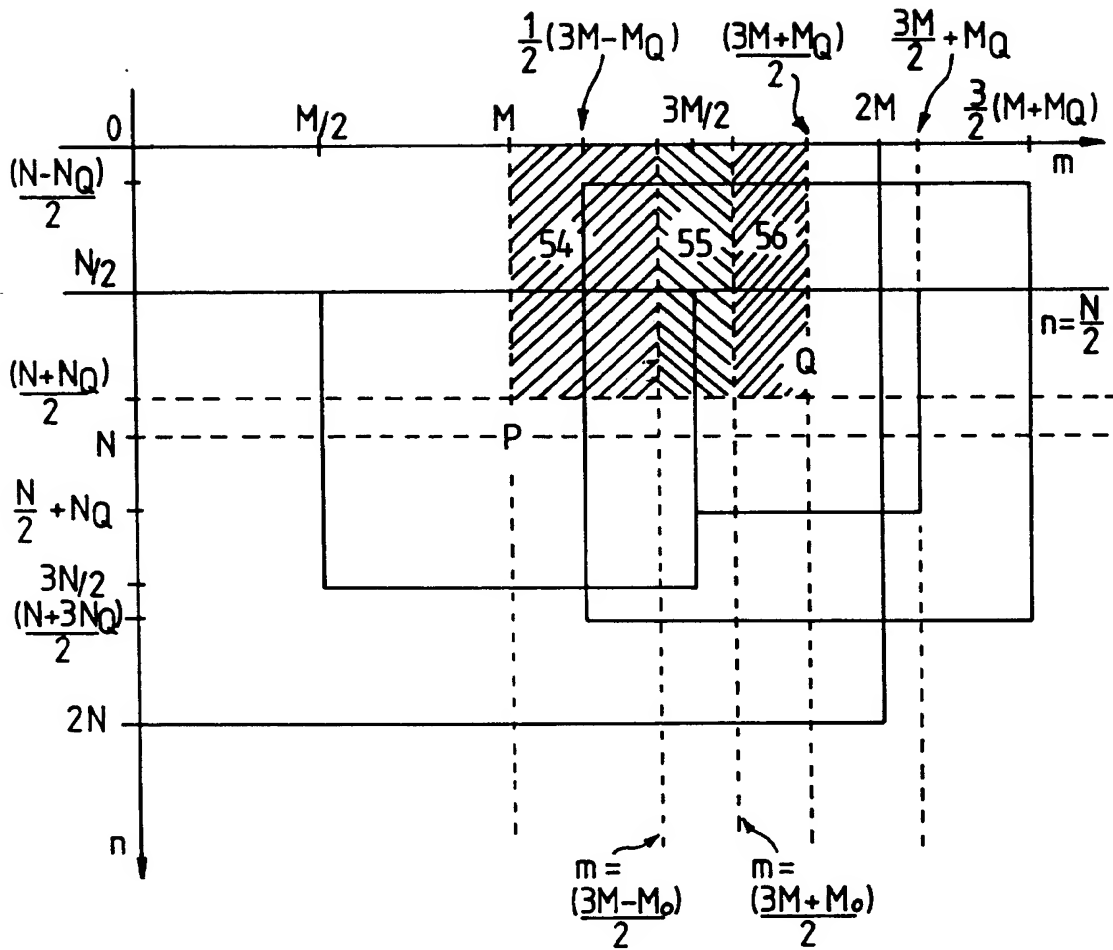
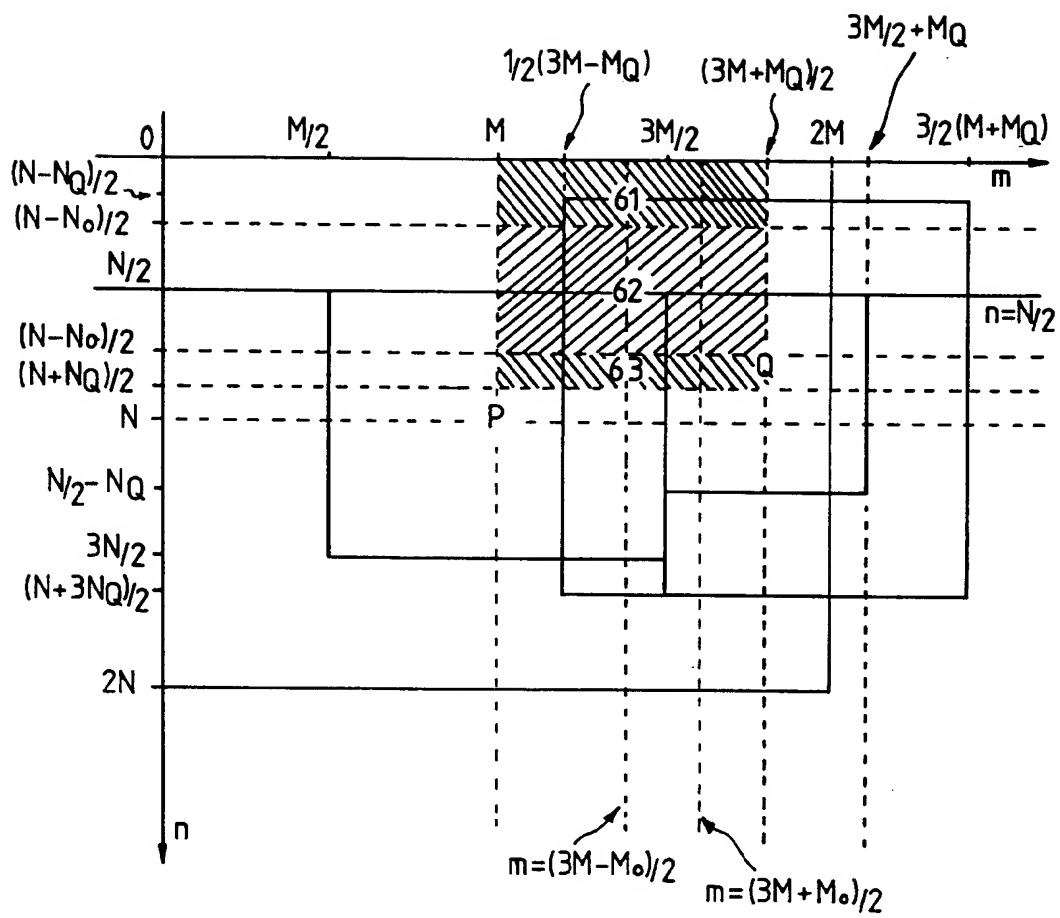


Fig. 5 c

Fig. 5d



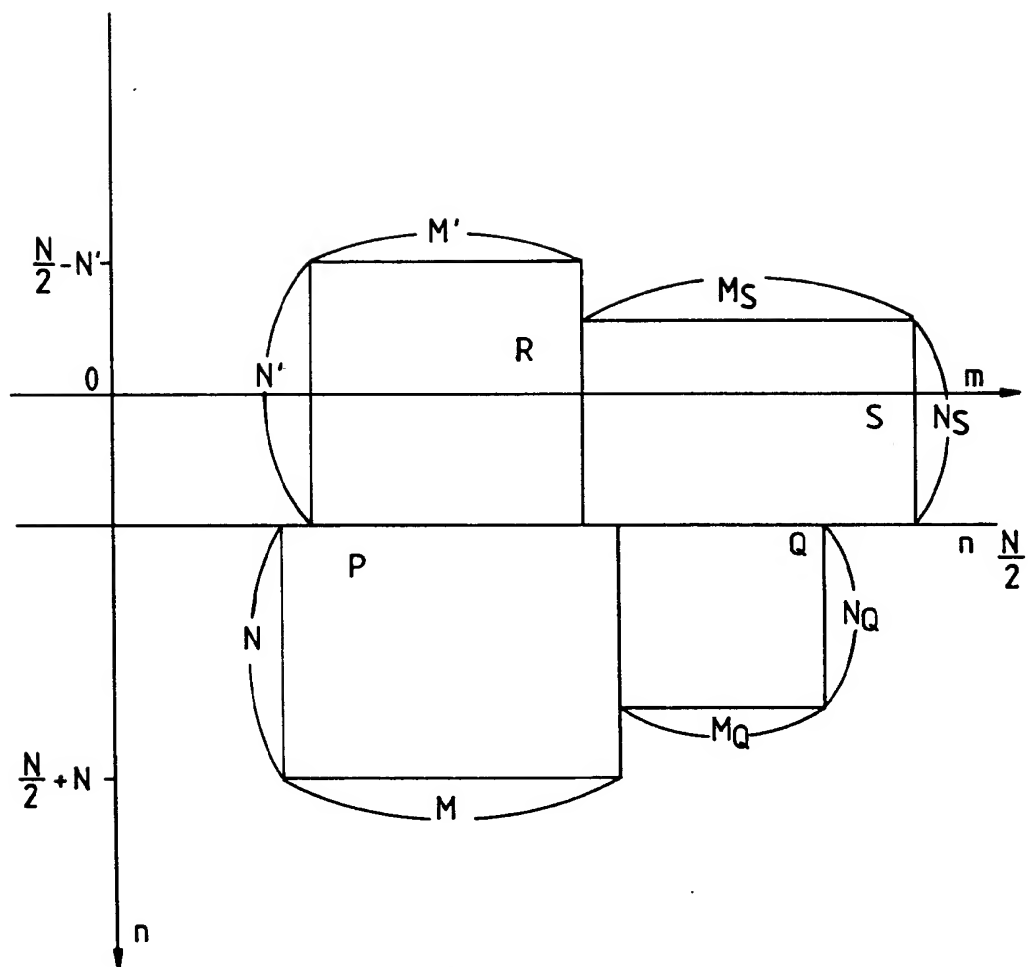
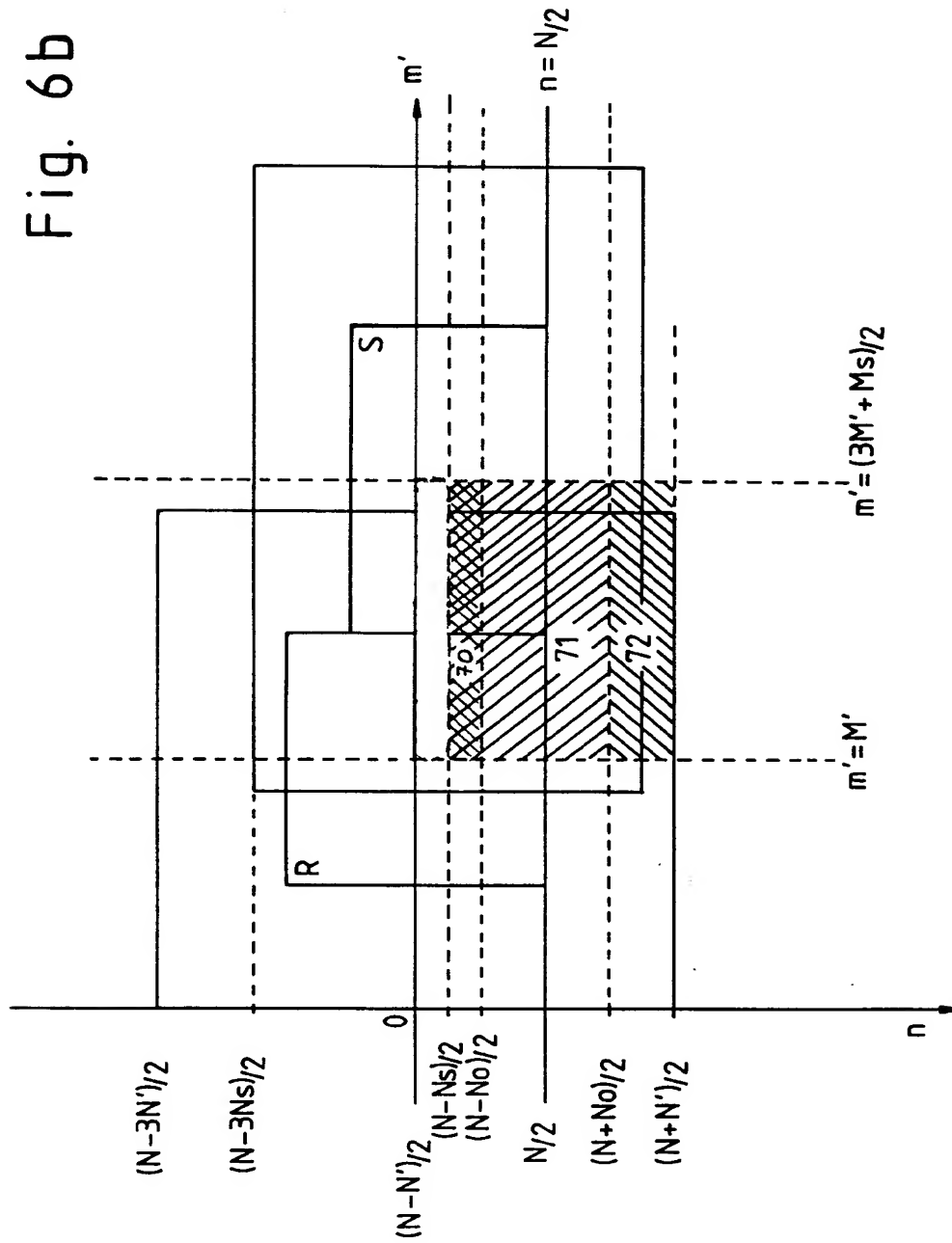


Fig. 6a

Fig. 6b



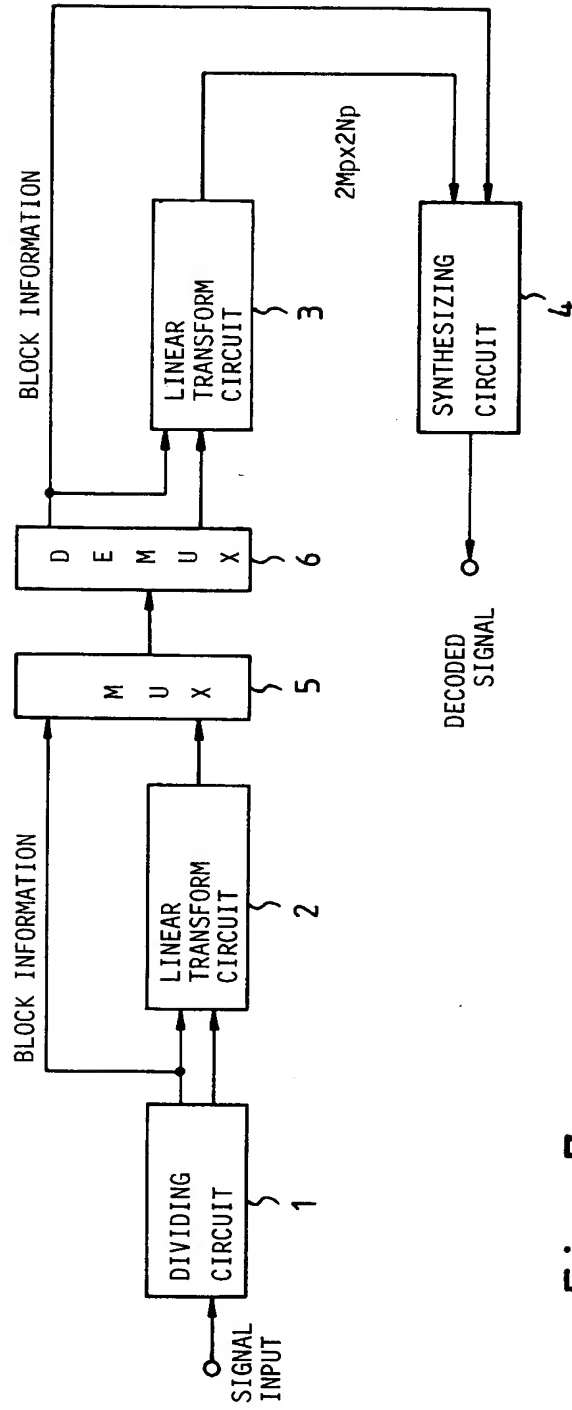
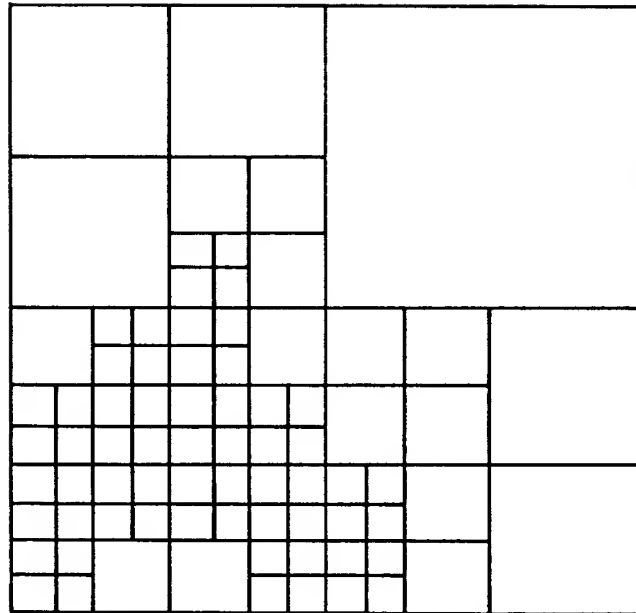


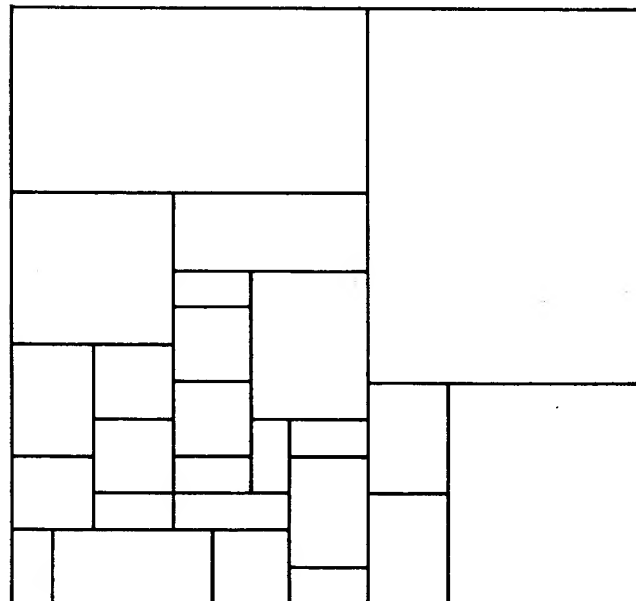
Fig. 7

Fig. 8

a



b



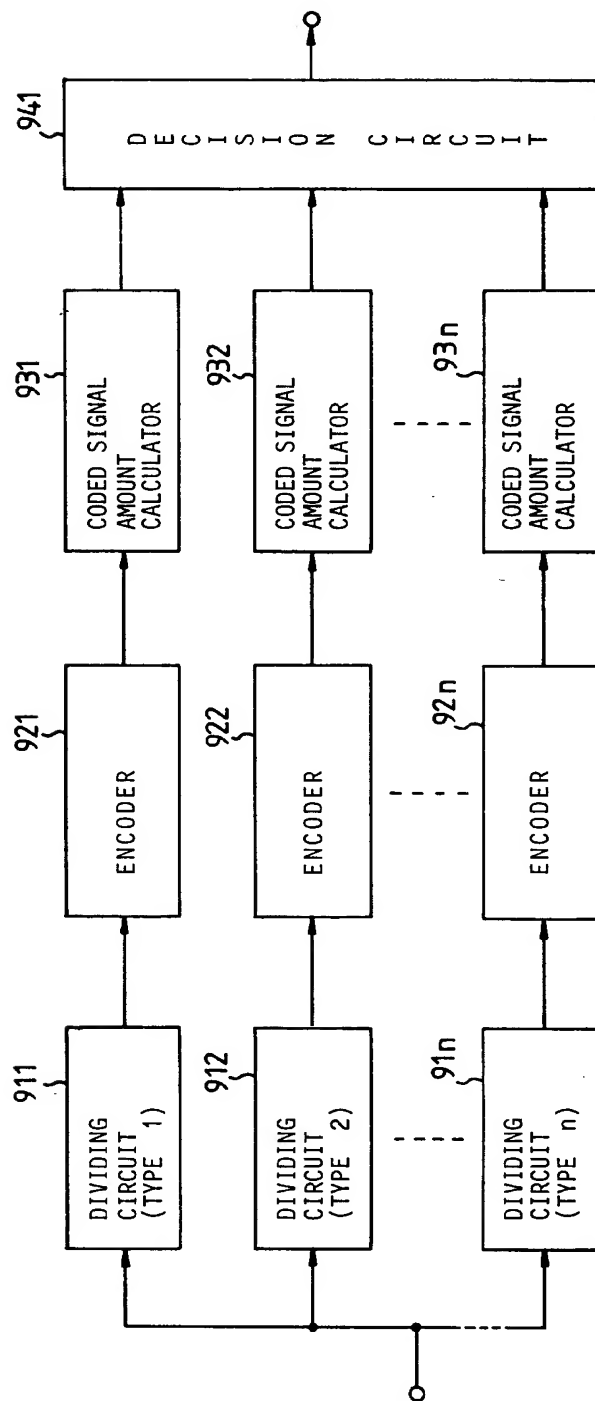


Fig. 9a

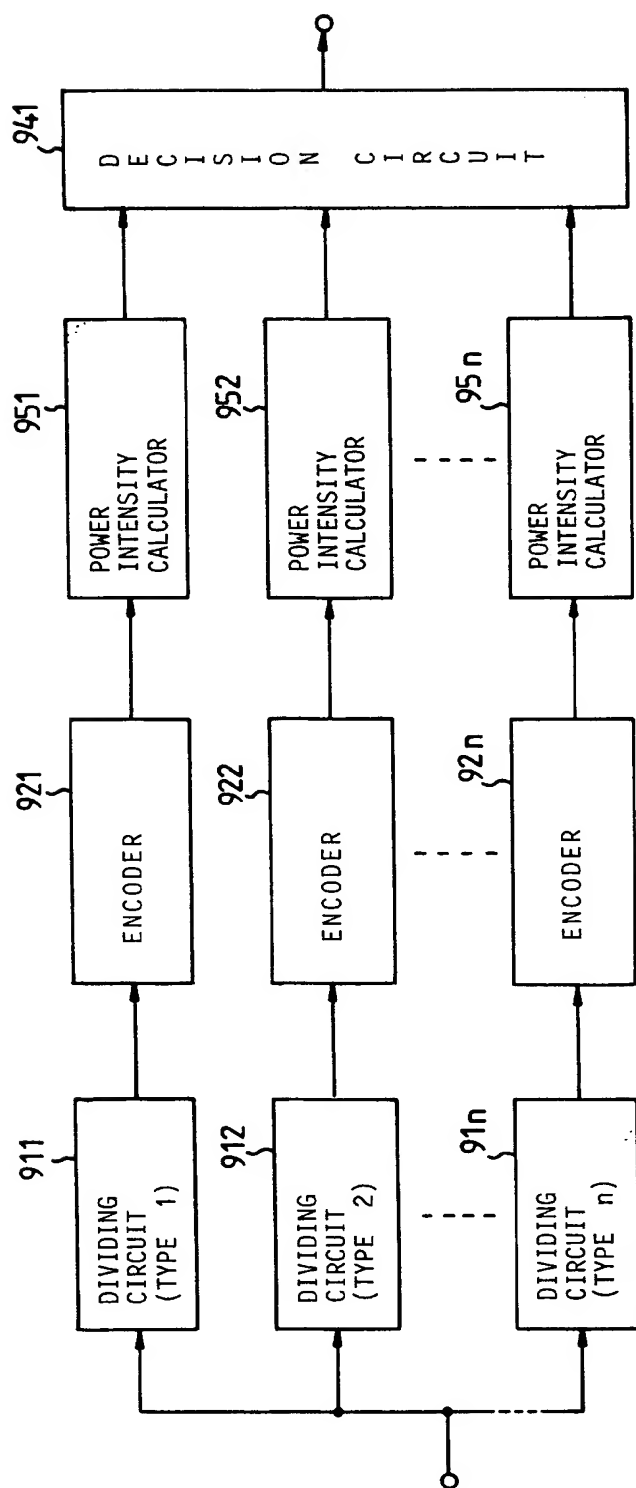


Fig. 9 b

Fig. 10 a

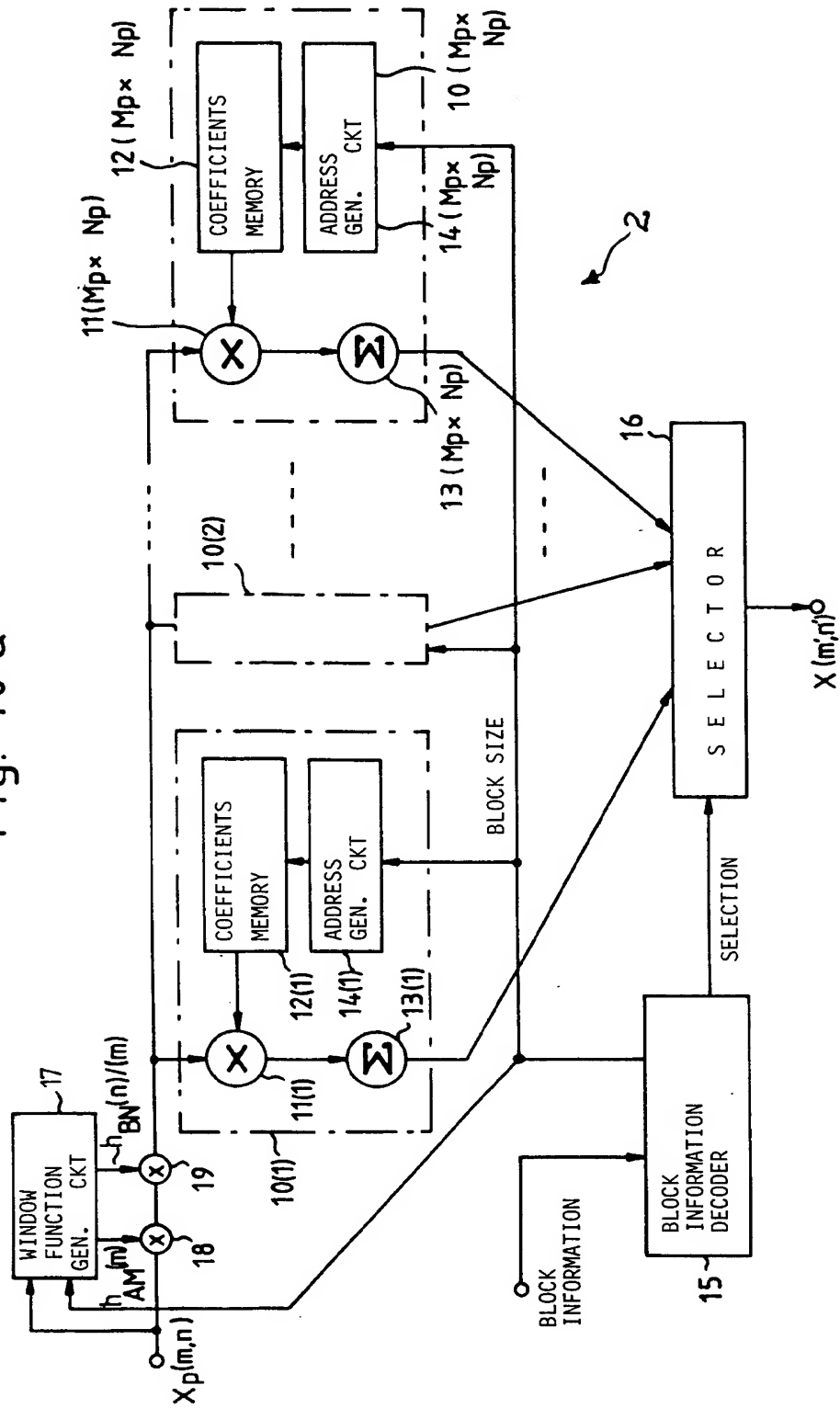
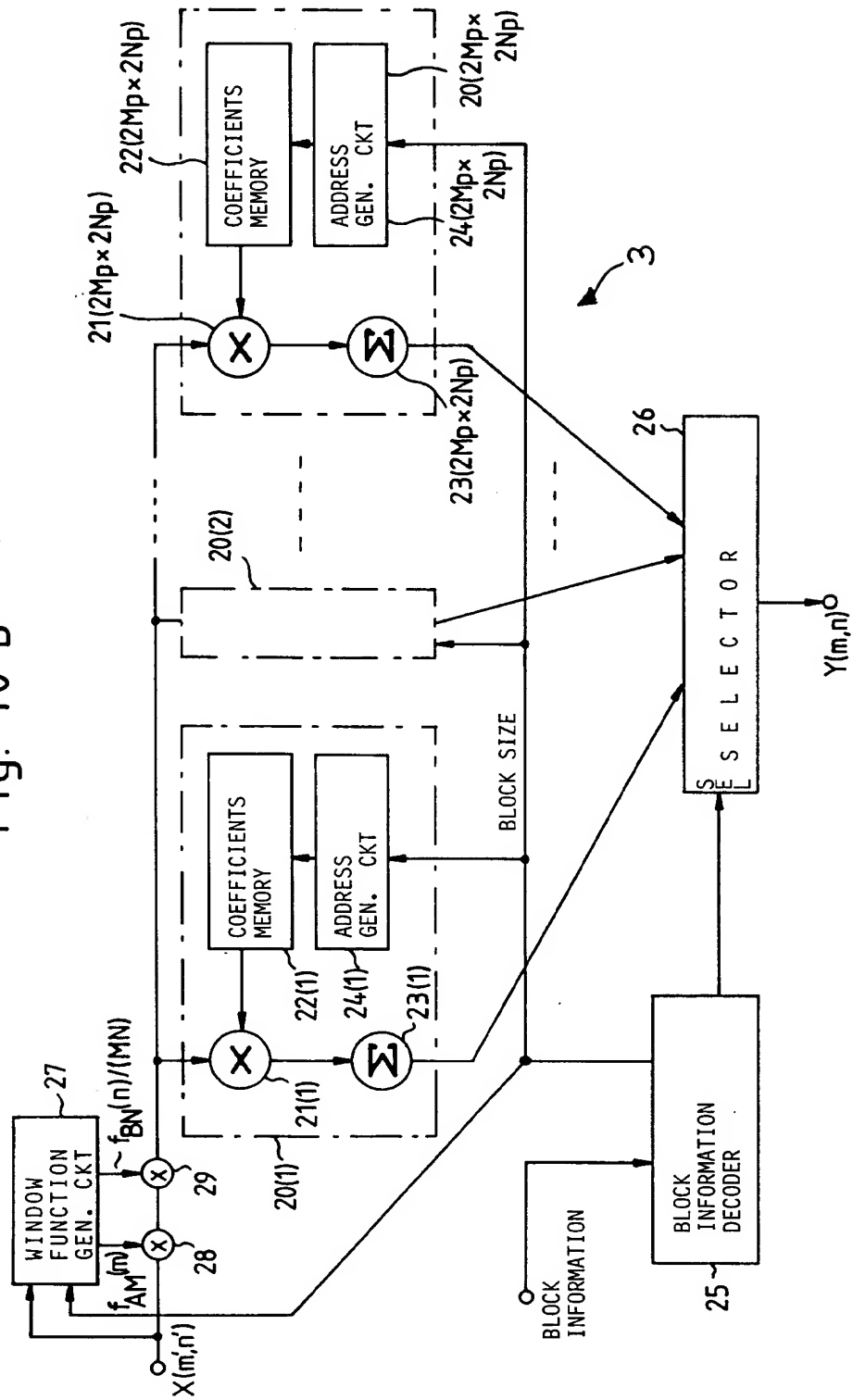


Fig. 10 b



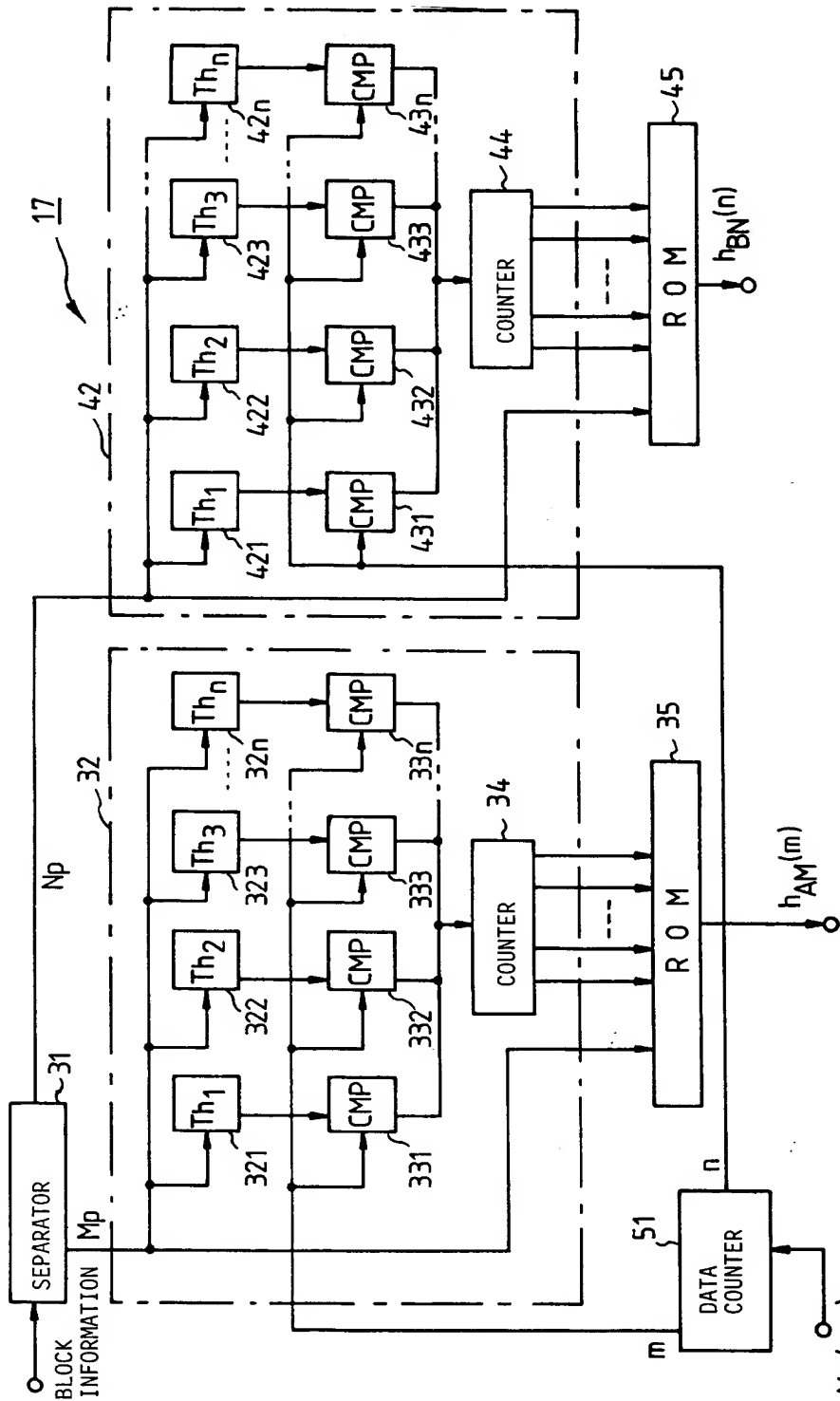


Fig. 11a

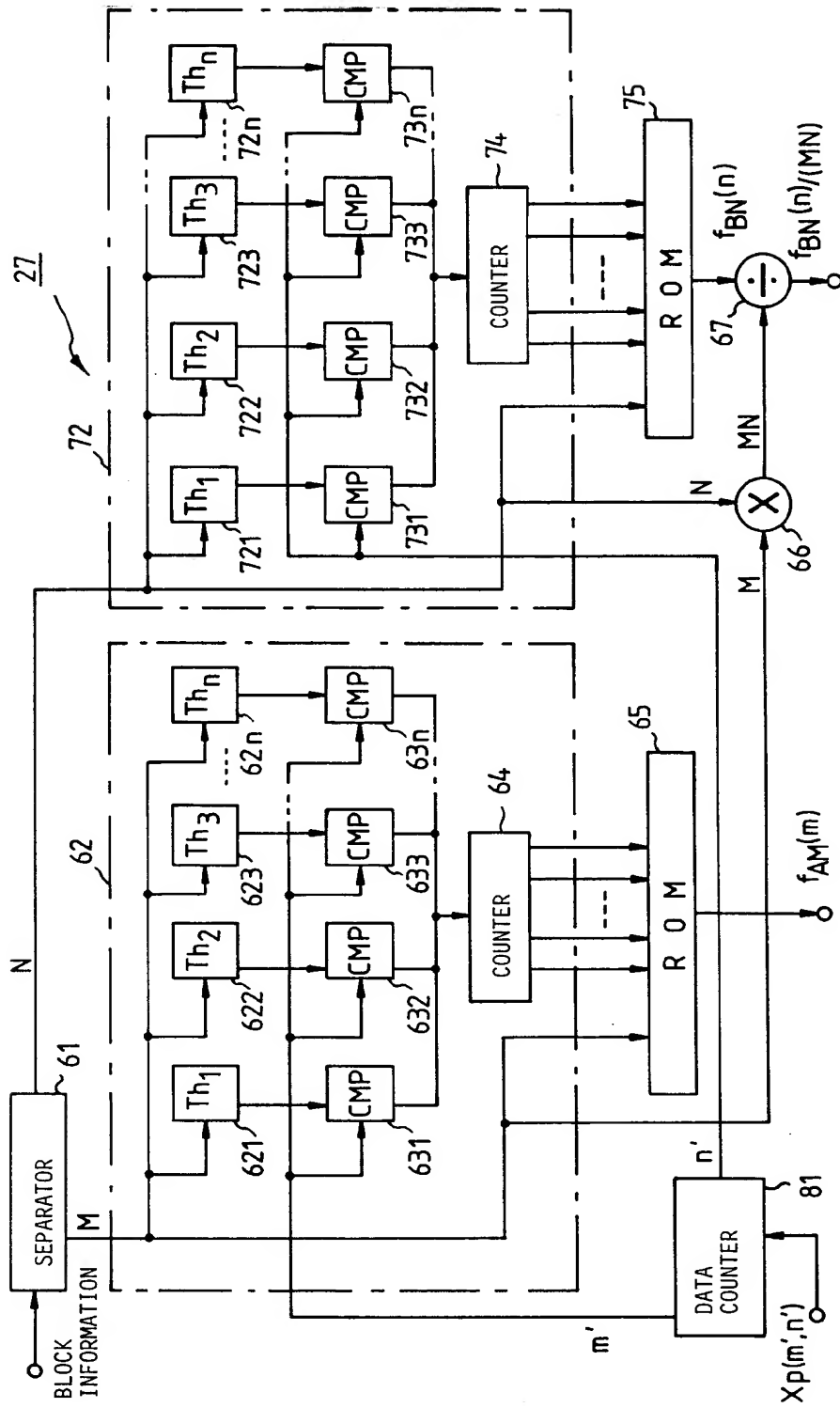


Fig. 11b

Fig. 12

R E G I O N S	C O U N T E R 34	O U T P U T
$m < (M-M_o)/2$	0000	
$(M-M_o)/2 \leq m < (M+M_o)/2$	0001	
$(M+M_o)/2 \leq m < (3M-M_o)/2$	0010	
$(3M-M_o)/2 \leq m < (3M+M_o)/2$	0011	
$(3M+M_o)/2 < m$	0100	

Fig. 13

A D D R E S S	R O M 35	O U T P U T
M	$h_{AM}(m) \quad (m < (M-M_o)/2)$	
M + 1	$h_{AM}(m) \quad ((M-M_o)/2 \leq m < (M+M_o)/2)$	
M + 2	$h_{AM}(m) \quad ((M+M_o)/2 \leq m < (3M-M_o)/2)$	
M + 3	$h_{AM}(m) \quad ((3M-M_o)/2 \leq m < (3M+M_o)/2)$	
M + 4	$h_{AM}(m) \quad ((3M+M_o)/2 < m)$	

Fig. 14

